

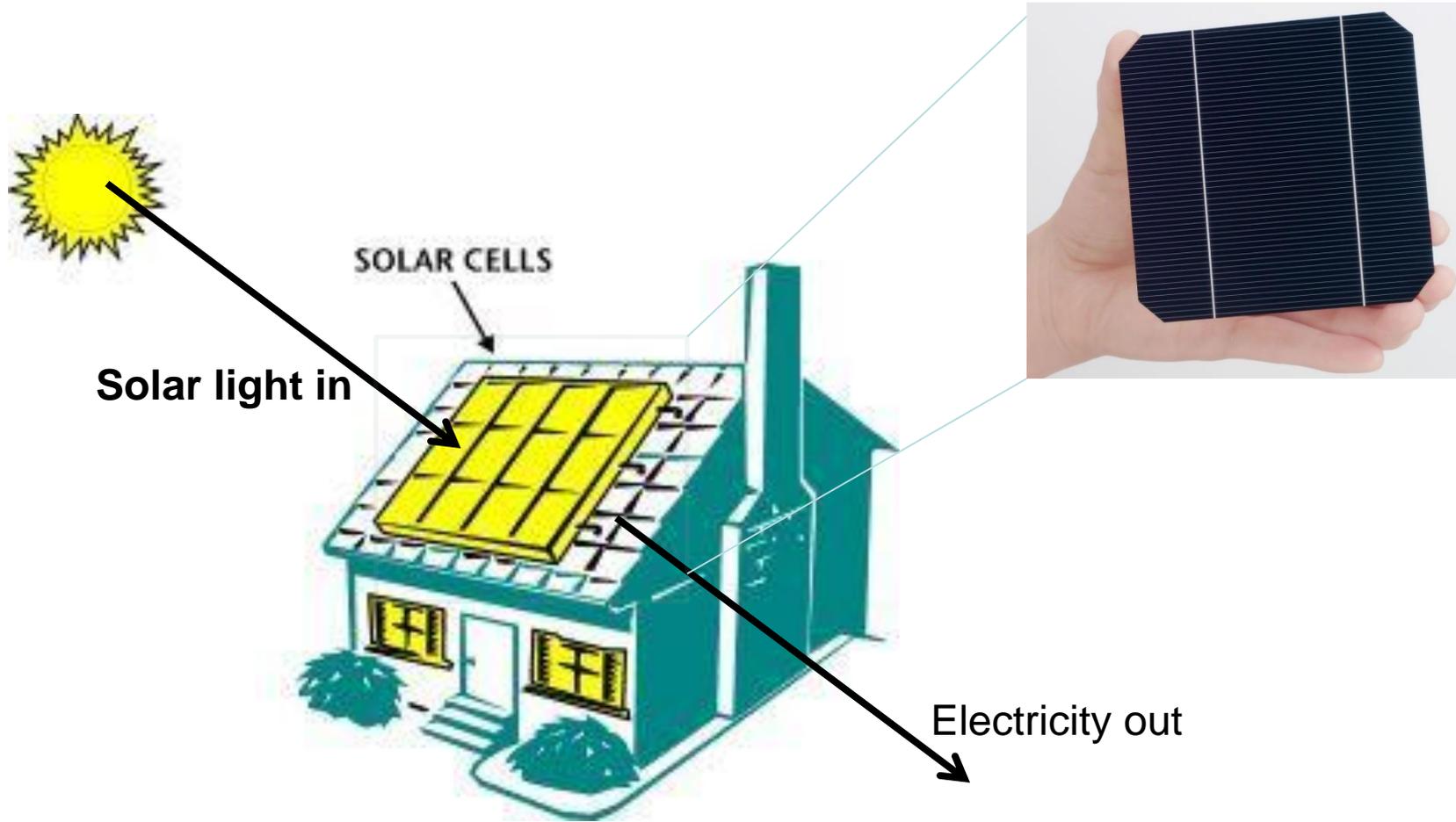
# 能源科技與環境概論

## 太陽能電池導論

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Tsing-Hua University

# Solar cells



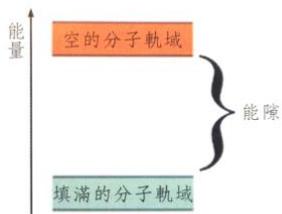
# Energy conversion of light

- $E(\text{energy}; J \text{ or } eV) = \frac{h(\text{Planck constant}) * c(\text{light speed})}{\lambda(\text{wavelength})}$
- $E = \frac{p^2}{2m}$
- $\lambda = \frac{hc}{E} = \frac{1240 \text{ eV nm}}{E (eV)}$
- $p(\text{momentum}) = hk(\text{wave factor})$

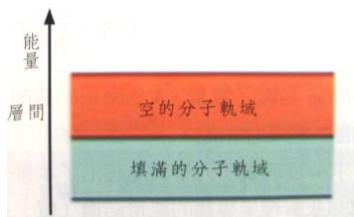
A light with a wavelength of 1100 nm corresponds to 1.12 eV



# Generate electrons from materials by photons



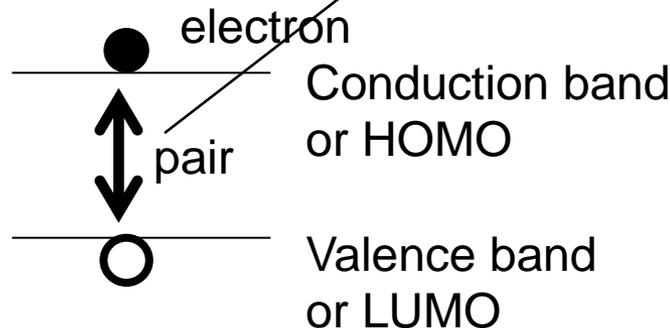
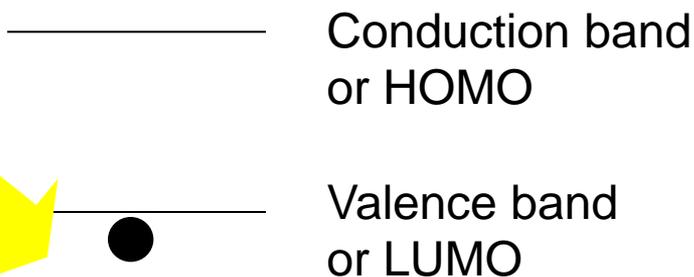
(b)



excited state

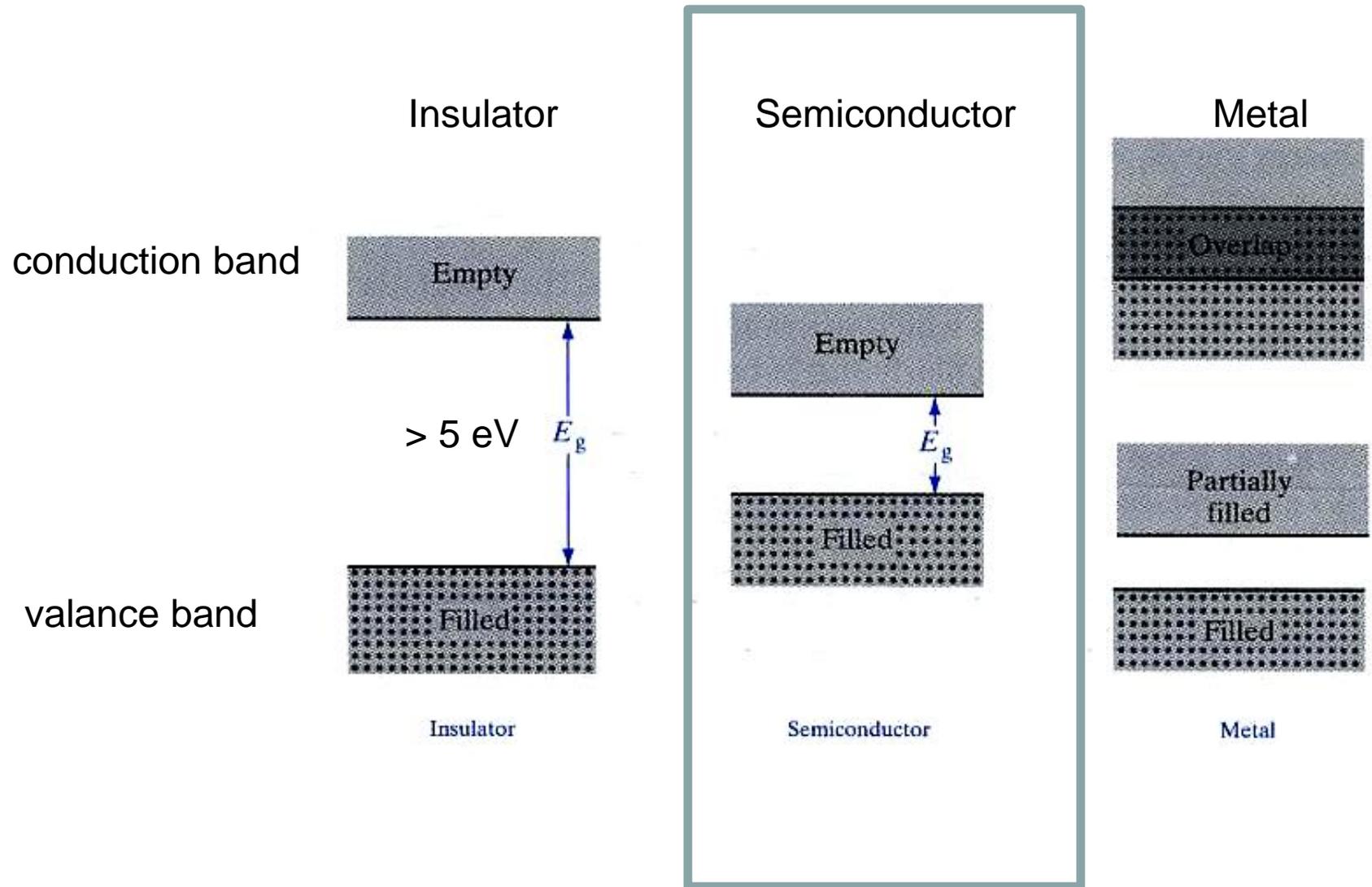


Separation of the electron-hole pair



So, materials used for solar cell application need to have discrete energy gap for photogeneration of electron-hole pair

# Inorganic materials: Insulator, semiconductor and metal

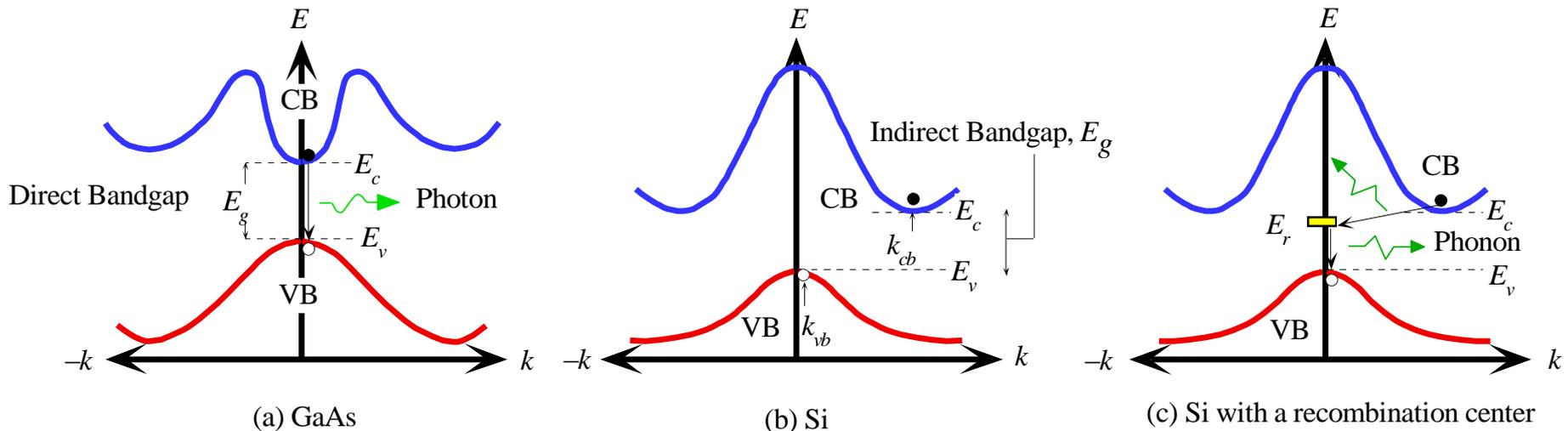


# Pick right materials as absorption layer

- Good absorption coefficient to harvest light
- Suitable band gap

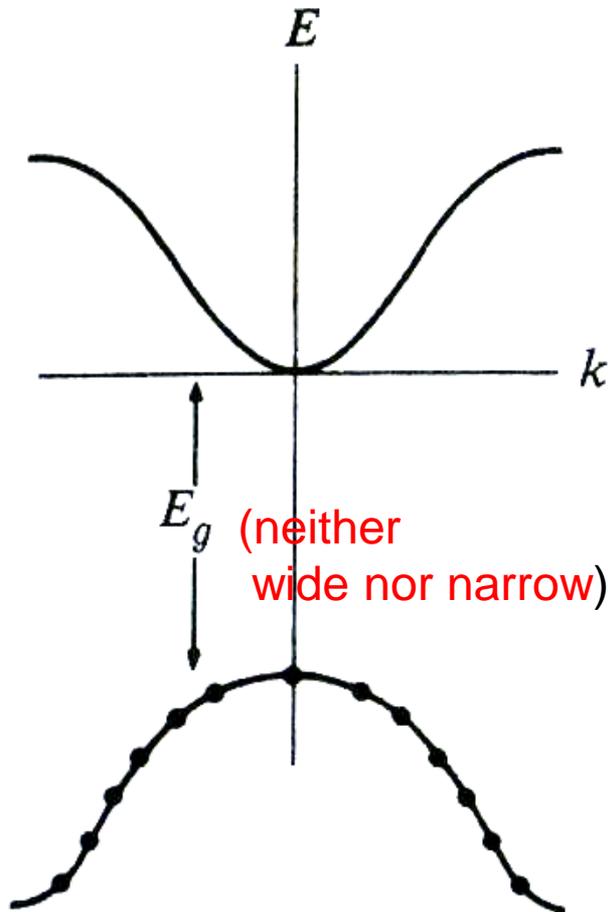
# Direct band gap and indirect band gap structure

-We prefer direct semiconductor materials since they can absorb light more efficiently



(a) In GaAs the minimum of the CB is directly above the maximum of the VB. GaAs is therefore a direct bandgap semiconductor. (b) In Si, the minimum of the CB is displaced from the maximum of the VB and Si is an indirect bandgap semiconductor. (c) Recombination of an electron and a hole in Si involves a recombination center.

# Pick suitable $E_g$ (eV)



Theoretical maximum efficiency of a semiconductor

$$\eta_{\max}(E_g) = \frac{E_g I_{\text{inc}}}{P_{\text{inc}}} = \frac{E_g \cdot \int_{\lambda < \lambda_G} S(\lambda) d\lambda}{\int_{\lambda < \lambda_G} \frac{hc}{\lambda} S(\lambda) d\lambda}$$

$S(\lambda)$  = # of photons/area\*time

-bandgap of semiconductor should not be wide in order to get higher  $\int_{\lambda < \lambda_G} S(\lambda) d\lambda$

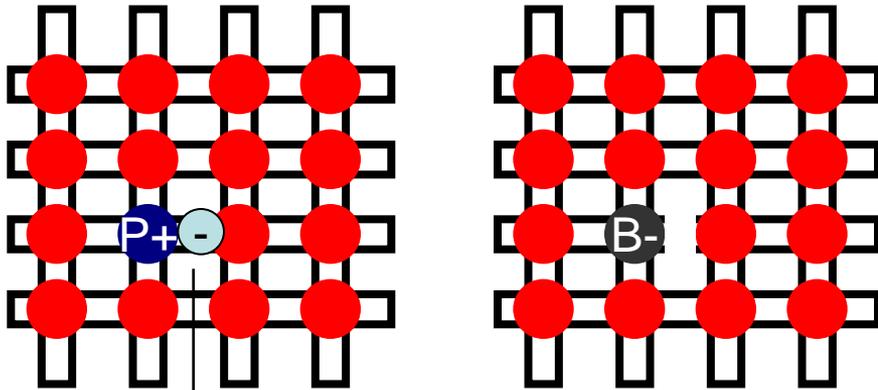
-Electrons in the valence band can absorb energy of  $E_g$ ,  $2E_g$ ,  $3E_g$ , but the excess energy can not be transformed to electric energy but transform to heat  $\rightarrow$  need higher  $E_g$

-very narrow band gap material can absorb most wavelength from sun, but transformed energy is small.

# Doping of Si: increase the conductivity of intrinsic Si

Donors: P, As, Sb (Column V elements), n-type, → provide one additional electron

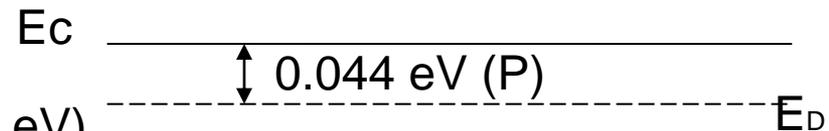
Acceptors: B, Ga, In, (Column III elements), p-type → provide one additional hole



-weakly bound

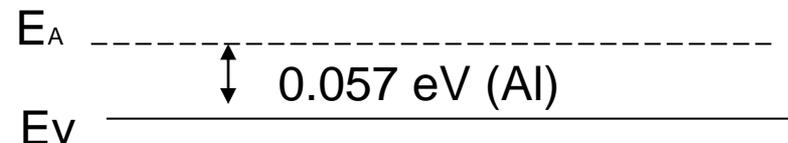
-bonding strength  $E_B \sim 0.05$  eV (Si bonding  $\sim 1.12$  eV)

Donors	$E_B$	Acceptors	$E_B$
Sb	0.039 eV	B	0.045 eV
P	0.044 eV	Al	0.057 eV
As	0.049 eV	Ga	0.065 eV
		In	0.16 eV

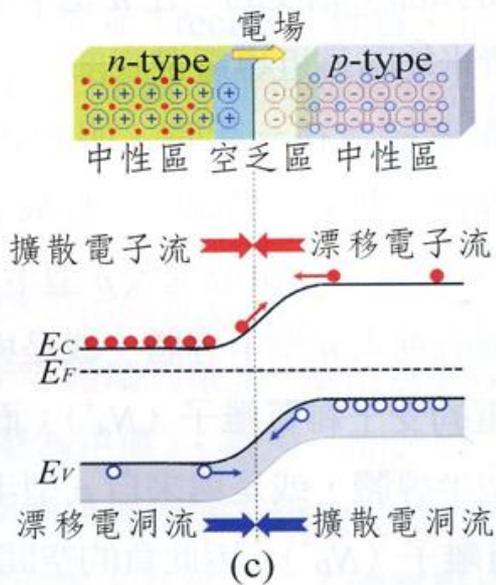
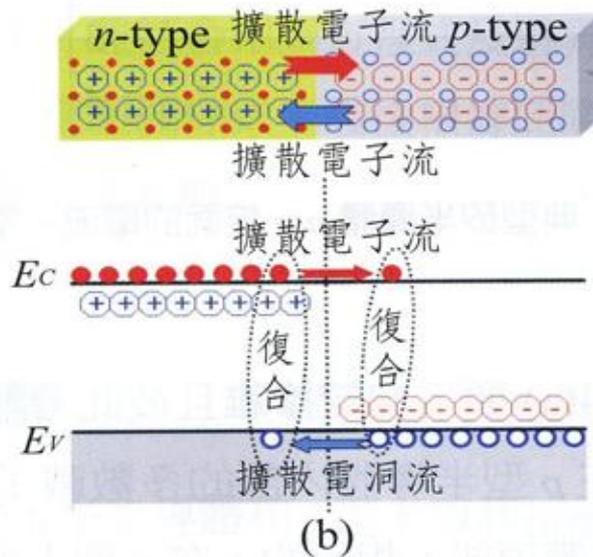
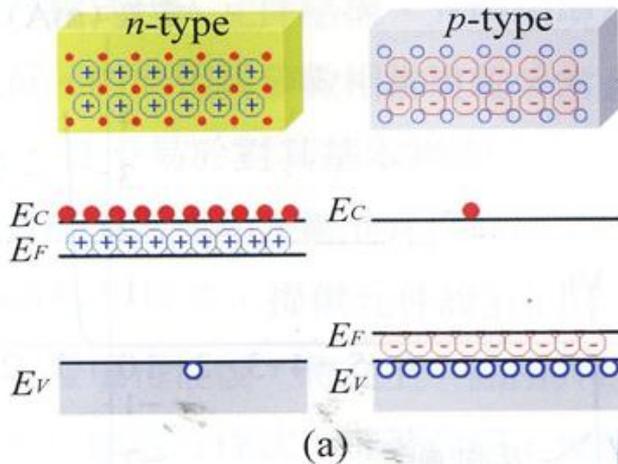


Majority carrier - electron in a n-type material  
hole in a p-type material

Minority carrier – hole in a n-type material  
electron in a p-type material

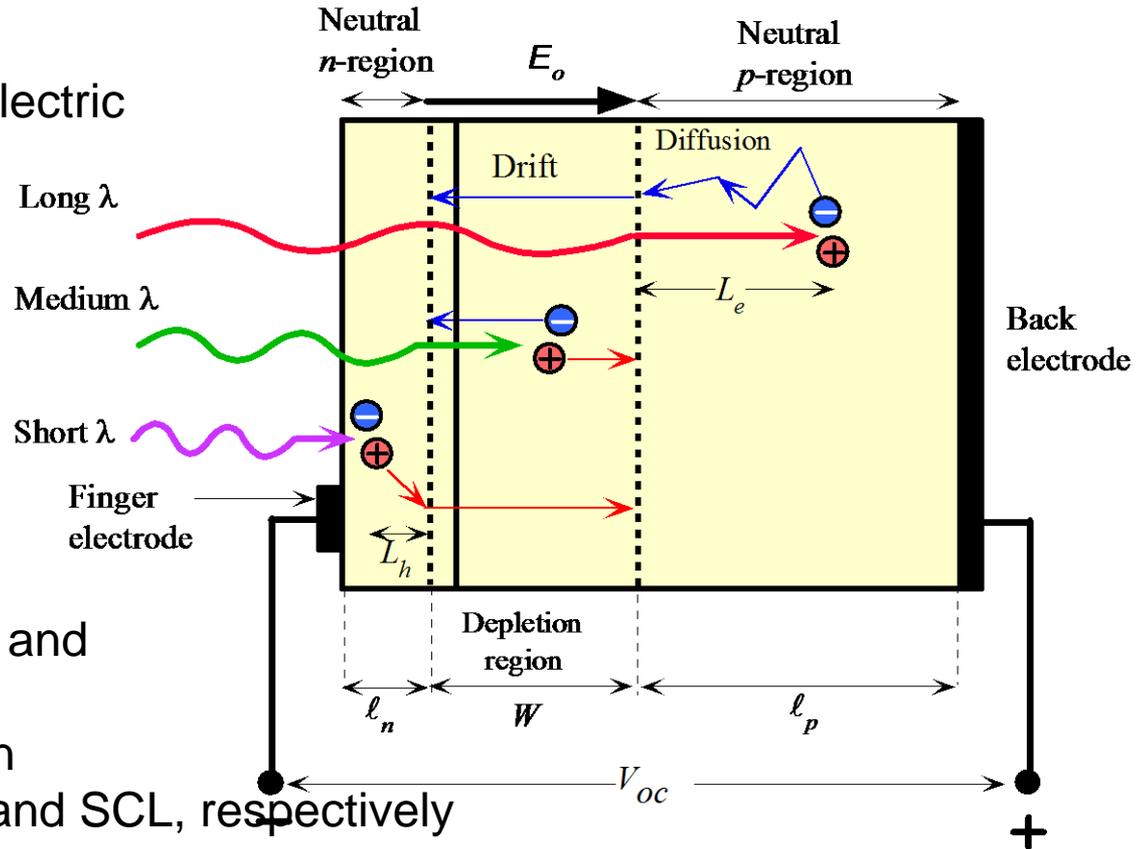


# P-N junction



# A PN junction photovoltaic device

PN junction provide a Depletion region in where an electric field is created for drifting electron and hole

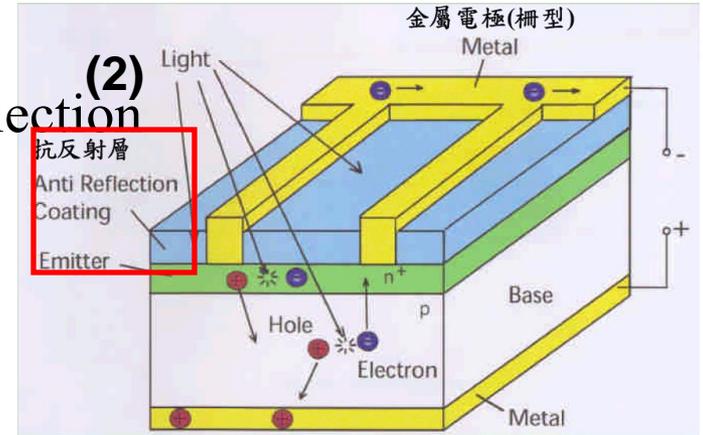
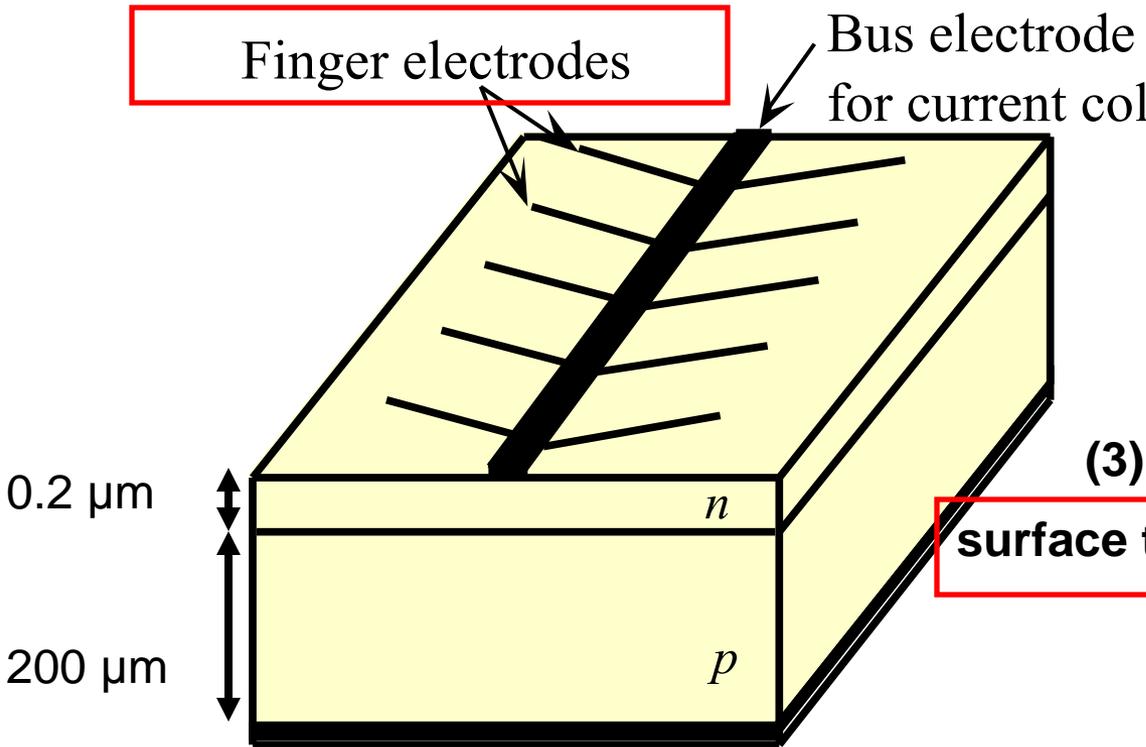


- incoming photon generate EHPs and create current
- generated electrons and hole can diffuse and drift in neutral region and SCL, respectively
- Opencircuit voltage ( $V_{oc}$ ) develops between the terminals of the device because the electron reaches the neutral n and p, respectively.

The principle of operation of the solar cell (exaggerated features to highlight principles)

# Device structure of a Si solar cell

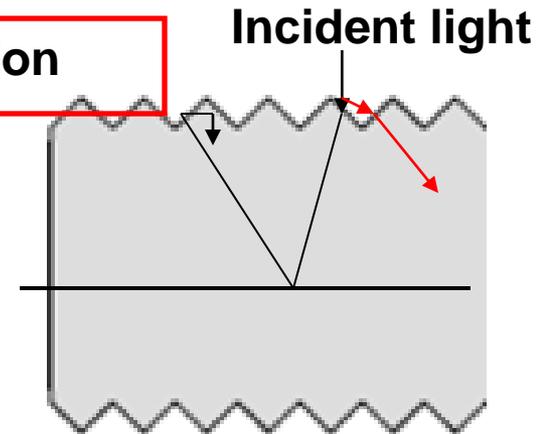
(1)



(2)

(3)

surface texturization



**In order to capture more light**

- finger electrodes were made to allow light pass through the device
- a thin antireflection coating on the surface reduces light reflection and allow more light to enter the device
- surface texturization to for multiple light reflection and increase light path

# Schematic of a semiconductor solar cell

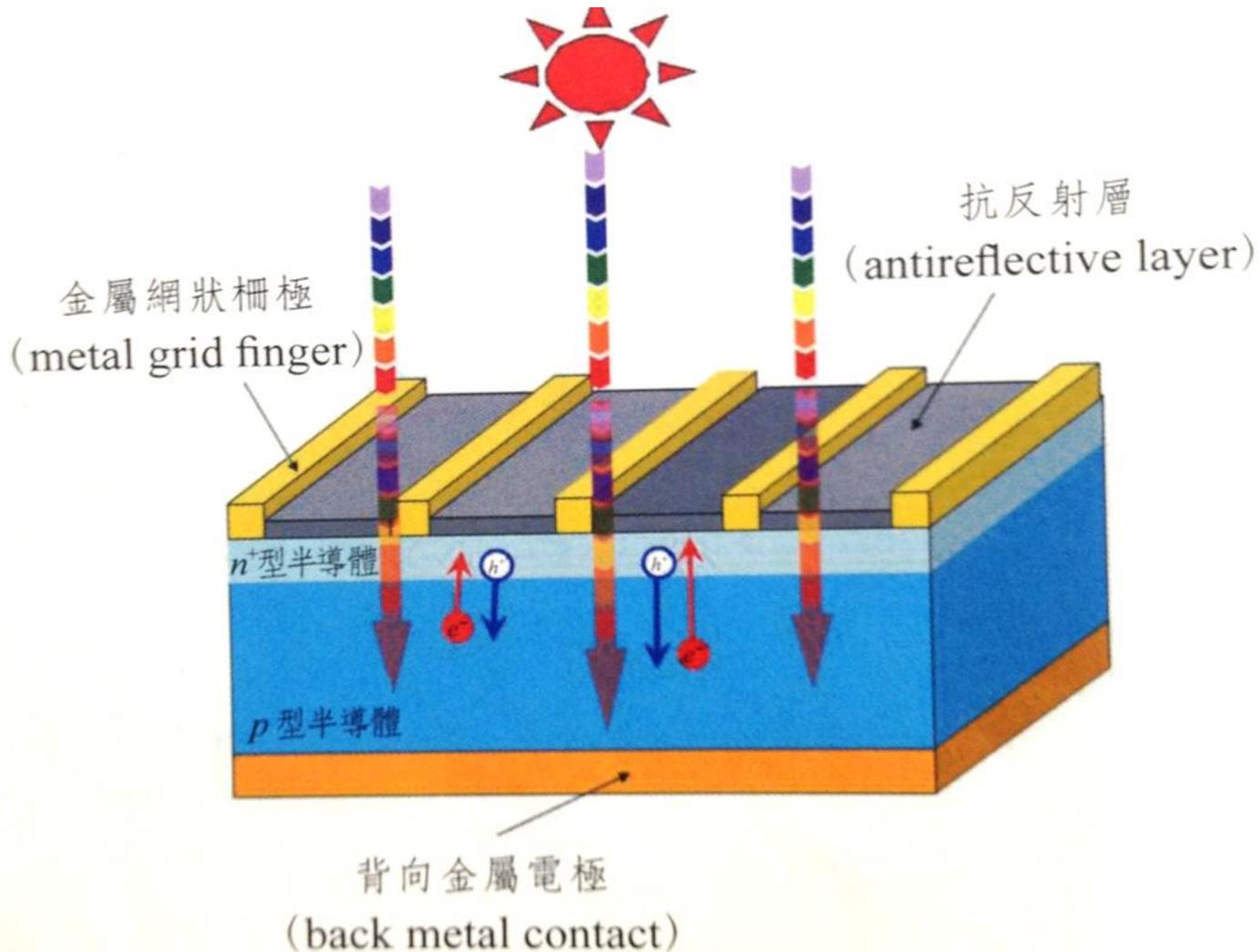
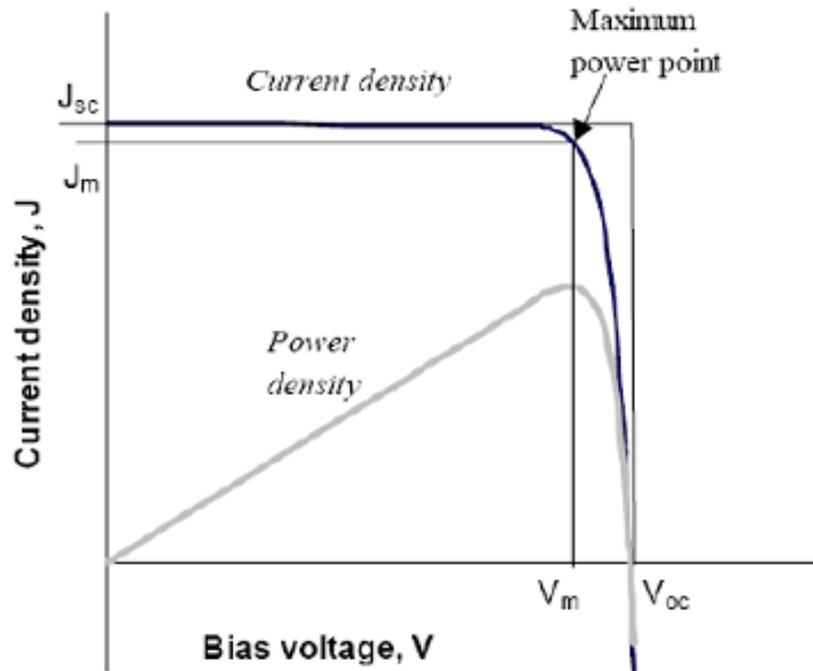


圖 2.1 半導體太陽電池結構示意圖

# Video

# IV curve of a solar cell



$$\eta = \frac{\text{Maximum PV output power}(P_{mp})}{\text{Incident solar power}(P_{in})} \times 100\%$$

$$= \frac{V_{mp} \times J_{mp}}{P_{in}} \times 100\%$$

$$= \frac{V_{oc} \times J_{sc}}{P_{in}} \times \frac{V_{mp} \times J_{mp}}{V_{oc} \times J_{sc}} \times 100\%$$

$$= V_{oc} \times J_{sc} \times FF / P_{in} \times 100\%$$

$$P_{mp} = J_{mp} \times V_{mp}; FF = \frac{J_{mp} \times V_{mp}}{J_{sc} \times V_{oc}}$$

## Voc (open circuit voltage)

-when output current approaches zero, the voltage develops between two terminals ideally  $V_{oc} \sim E_g$  at 0K and inverse proportional to temperature

## Jsc (short-circuit current)

-like the device connect the device with metal close to photogenerated current

**FF (fill factor): We want FF close to 1**

$\eta$  : Efficiency

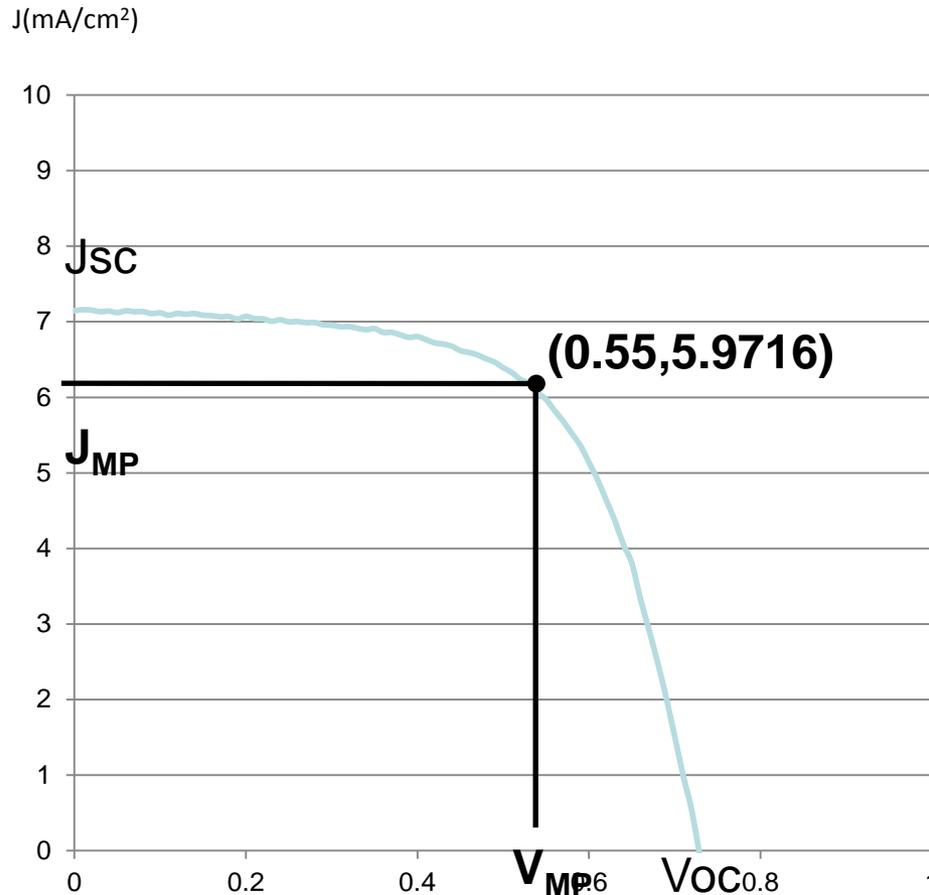
$V_{oc}$  : Open Circuit Voltage

$J_{sc}$  : Short Circuit Current Density

F.F. : Fill Factor

$P_{in}$  : incident solar power ( 1000W/m<sup>2</sup> or 100mW/cm<sup>2</sup>)

# Solar cell efficiency :an example



$\eta$  and FF in this device ?

$$\eta = P_{MP} / P_{in} \times 100\% = FF * V_{oc} J_{sc} / P_{in} \times 100\%$$

(輸出電功率/入射光功率)

$$P_{in} = 100 \text{ mW/cm}^2$$

$$P_{MP} = V_{MP} * J_{MP} = 0.55 * 5.9716 = 3.28 \text{ mW/cm}^2$$

$$\eta = 3.28 / 100 * 100\% = 3.28\%$$

$$V_{oc} = 0.72 \text{ V}$$

$$J_{sc} = 7.1464 \text{ mA/cm}^2$$

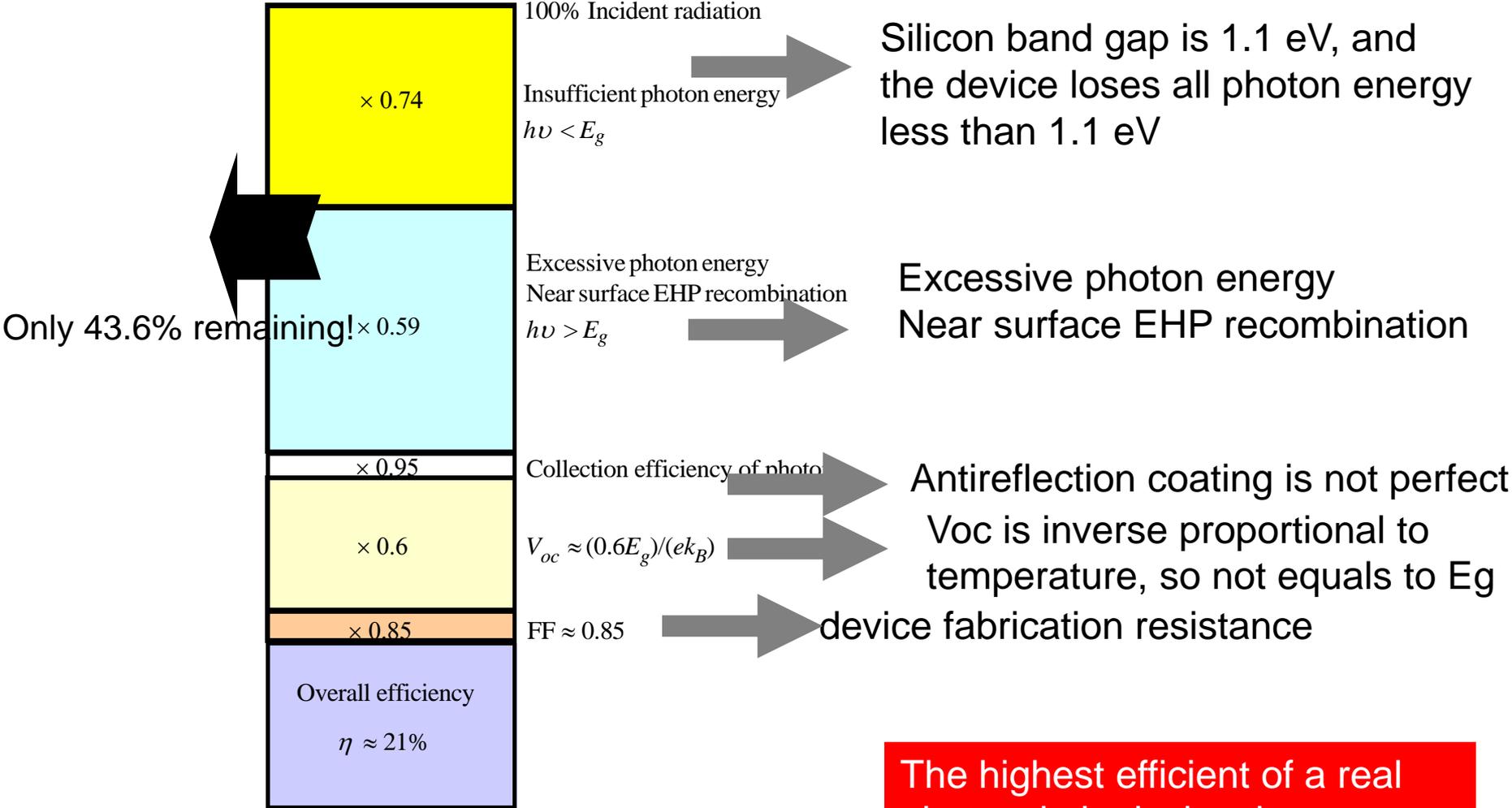
$$FF = V_{MP} * J_{MP} / V_{oc} * J_{sc}$$
$$= 3.28 / (0.72 * 7.14) = 0.63$$

**Voc:** 開路電壓 (open circuit voltage), 當輸出電流趨近於零, 相對太陽電池兩電極端點沒有連接所得到的電壓

**Jsc:** 短路電流 (short circuit current)

如將照光的pn二極體兩端的金屬電極用金屬線連接, 造成短路, 此短路電流等於光電流

# Various losses of solar energy of a Si solar cell during processing



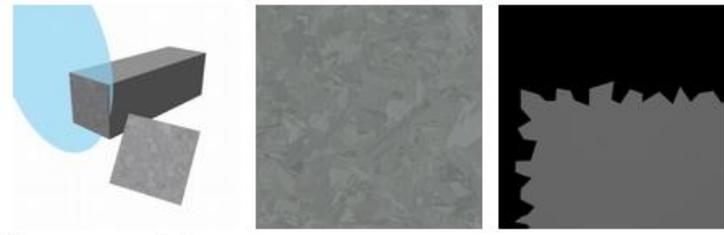
Accounting for various losses of energy in a high efficiency Si solar cell. Adapted from C. Hu and R. M. White, *Solar Cells* (McGraw-Hill Inc, New York, 1983, Figure 3.17, p. 61).

?1999 S.O. Kasap, *Optoelectronics* (Prentice Hall)

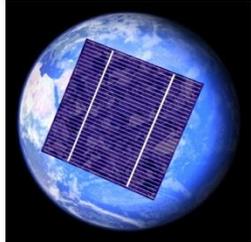
The highest efficient of a real photovoltaic device that uses a single crystal of Si is about 24.7% (澳洲新南威爾斯大學)

# Single/poly crystalline Si solar cell fabrication process

**Process Introduction** P-type ingot was cut



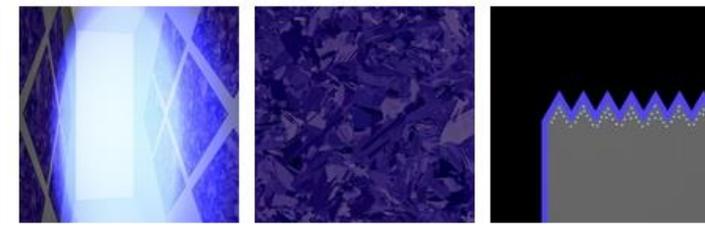
**Emitter Diffusion** (the wafer are placed in a belt furnace And heated about 900 °C



**Damage Etch** etch in a strong acidic or alkaline solution



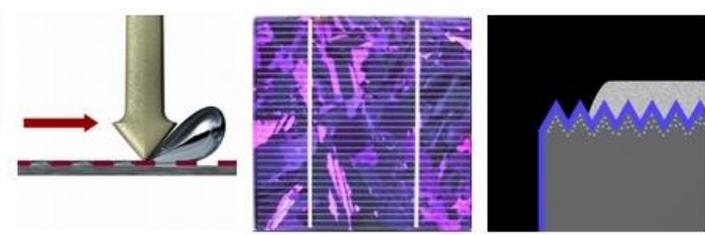
**Anti Reflection Coating** The reflectivity is still round 30%, H:SiNx is deposited on the cell to reduce more the reflectivity



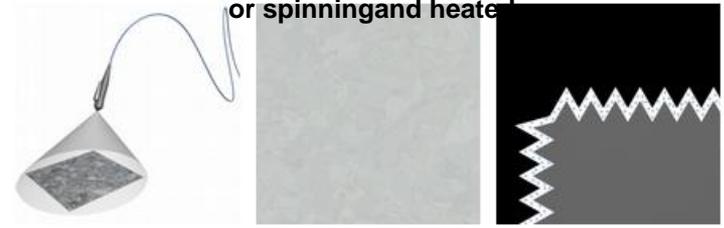
**Texturing** etch in a weak acidic or alkaline solution



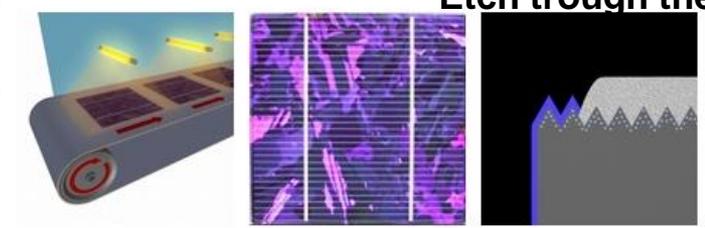
**Contact Formation** metal contacts are made by screen printing



**P-doping** liquid glass containing P is distributed on one side of the wafer by spraying or spinning and heated

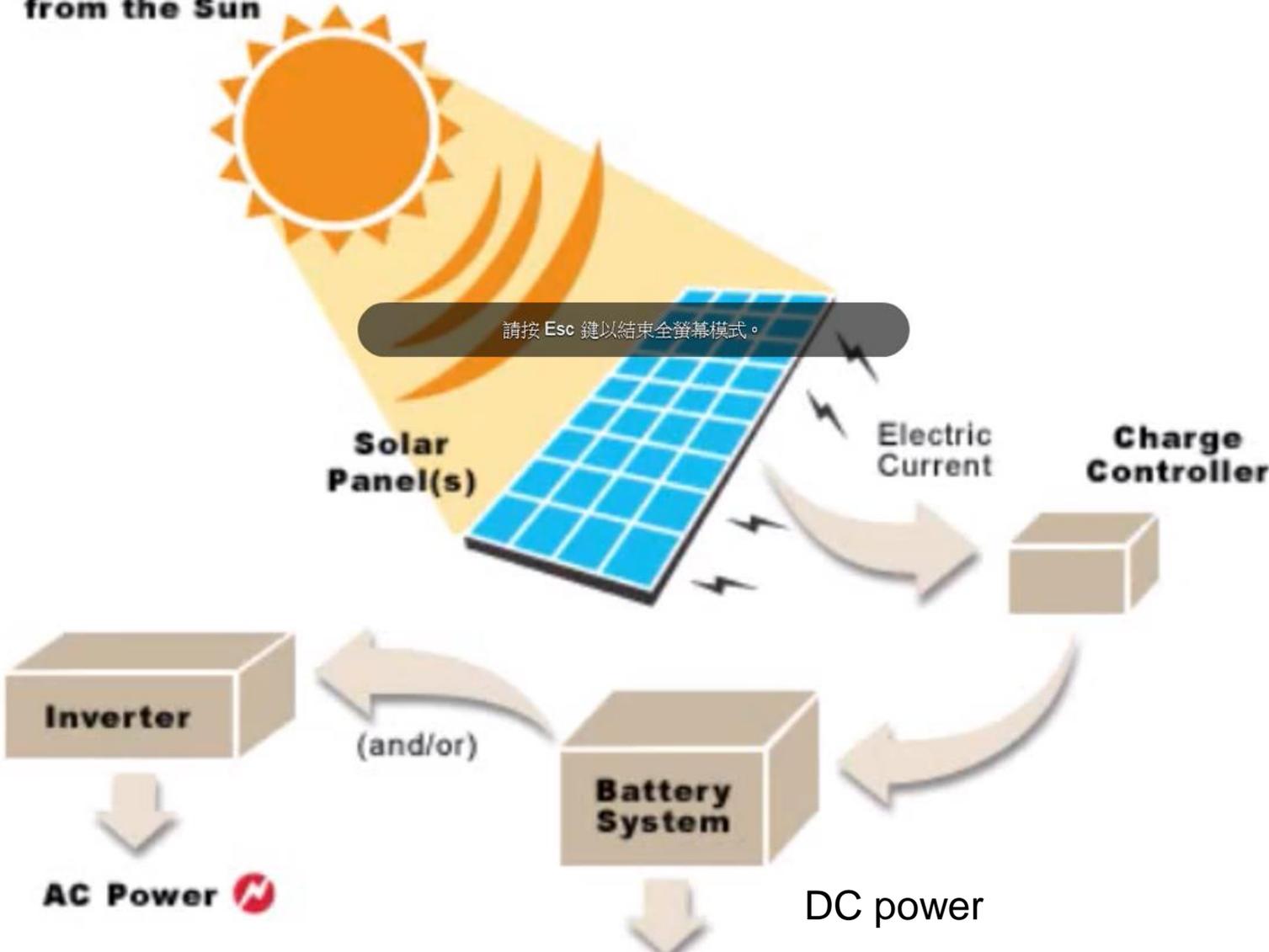


**Sintering and Firing** Metallic contacts are heated and Etch trough the antiflecton coating



# Solar cells installation

Solar irradiance  
from the Sun

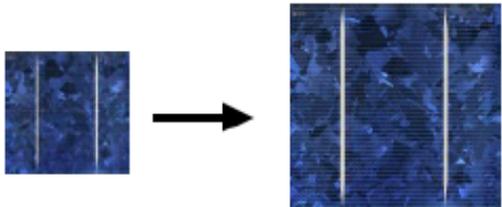


# Next step of crystalline Si solar cell

## Vision

Less Si:  
thickness from 300 to 150  $\mu\text{m}$

Larger:  
from 125 to 210 mm

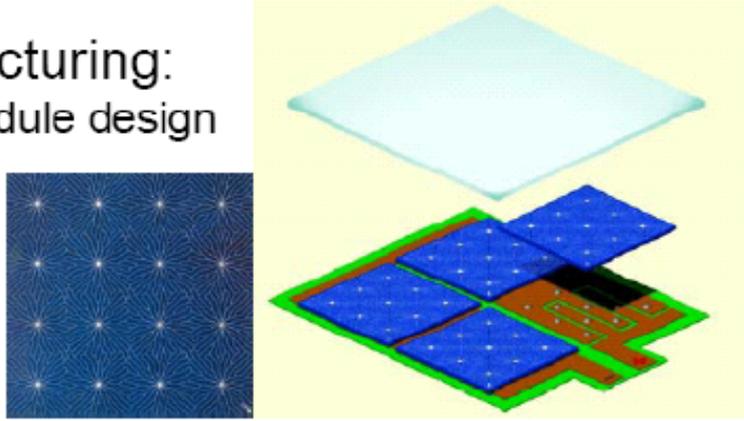


**Cost reduction mc-Si PV technology**

Efficiency:  
from 15 to 18%

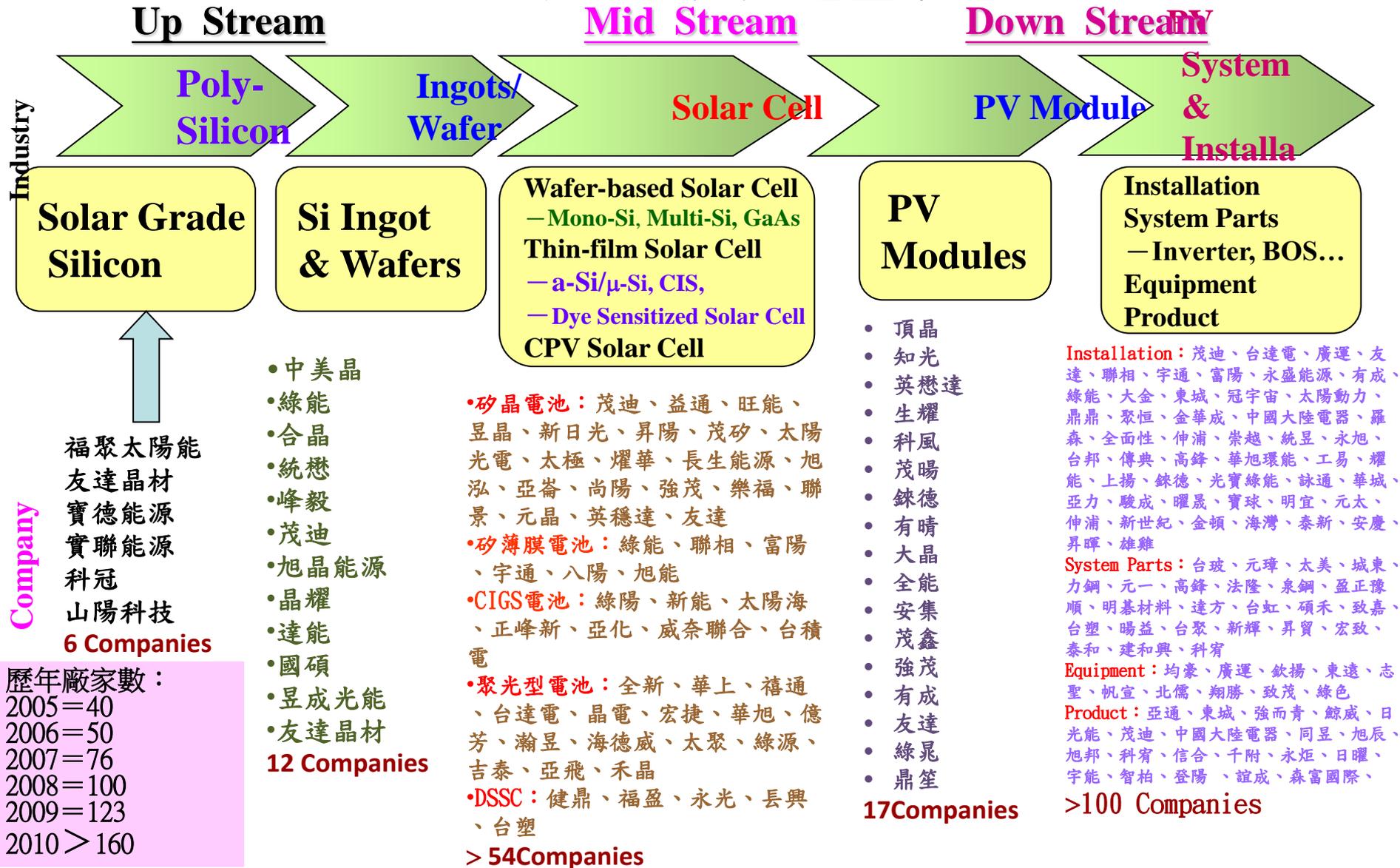


Easy manufacturing:  
new cell and module design

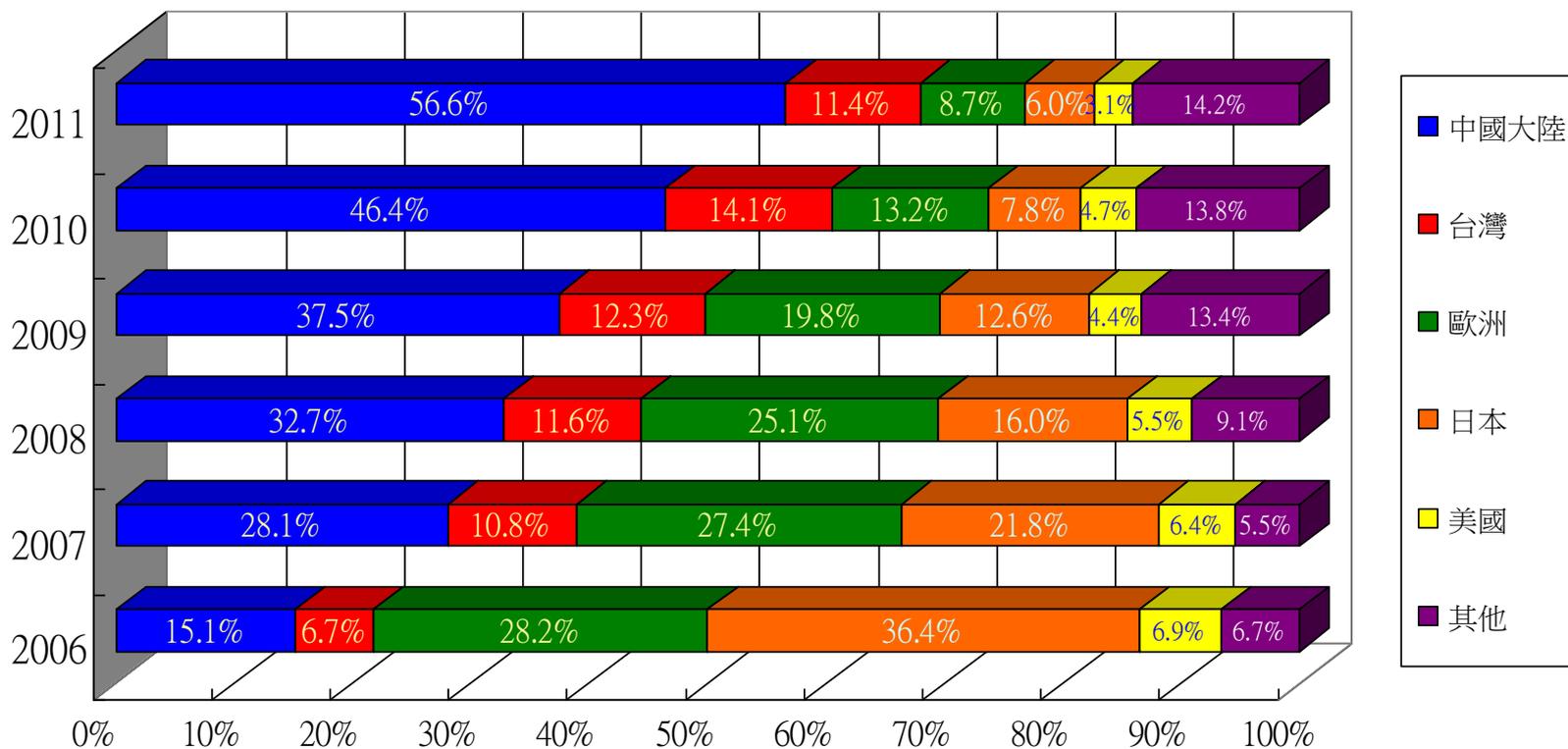


# 貳、發展現況(續)

## 五、我國太陽光電產業鏈



- 歐洲與日本之電池產量由2006年市占率約30%下降至2011年的10%以下。
- 太陽光電生產基地持續向亞洲移動趨勢不變，中國大陸與臺灣生產比例近全球70%。
- 大陸挾其成本優勢與政策支持，成為全球最大製造國，2011年產值達2536億元人民幣。



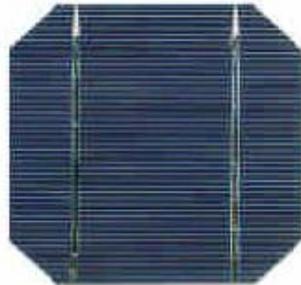
# Thin film solar cell: use Si as an example

c-Si (wafer technology)



solar module  
( $\eta = 11 - 15 \%$ )

c-Si solar cell



Si-thickness  
**200 - 300  $\mu\text{m}$**

a-Si thin film  
technology

Si-thickness  
**0.5  $\mu\text{m}$**

solar module  
( $\eta = 5 - 7 \%$ )

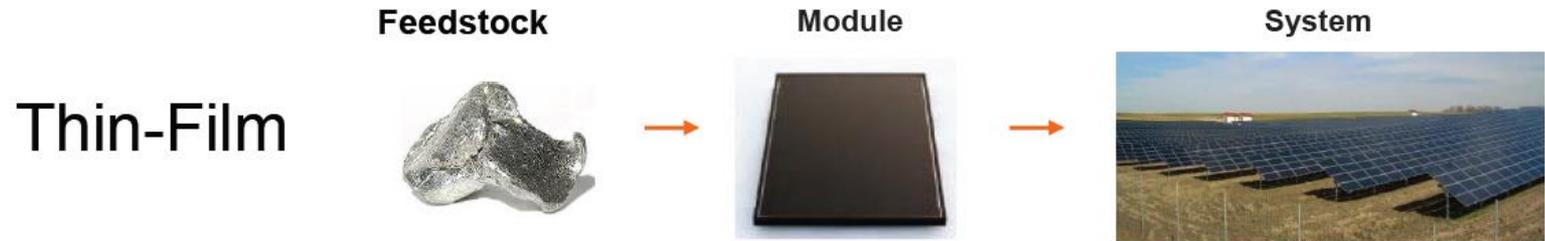


- required thickness of thin film solar cell is around 0.5  $\mu\text{m}$ , 1/500 of that of wafer based solar cell
- material cost is very low

# Advantages of thin film solar cells

1. low raw materials are required for fabrication
2. light transmission is better
3. more competitive price (CdTe, US\$1 per watt ; First Solar, stock price is US\$180 )
4. frameless design
5. Ideal for BIPV( building integrated photovoltaic)

# Advantage of Thin-Film

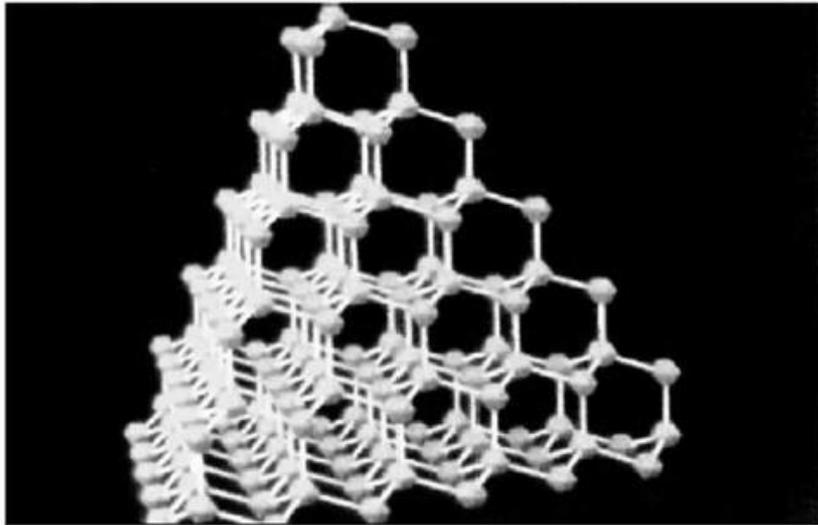


# Crystalline Si

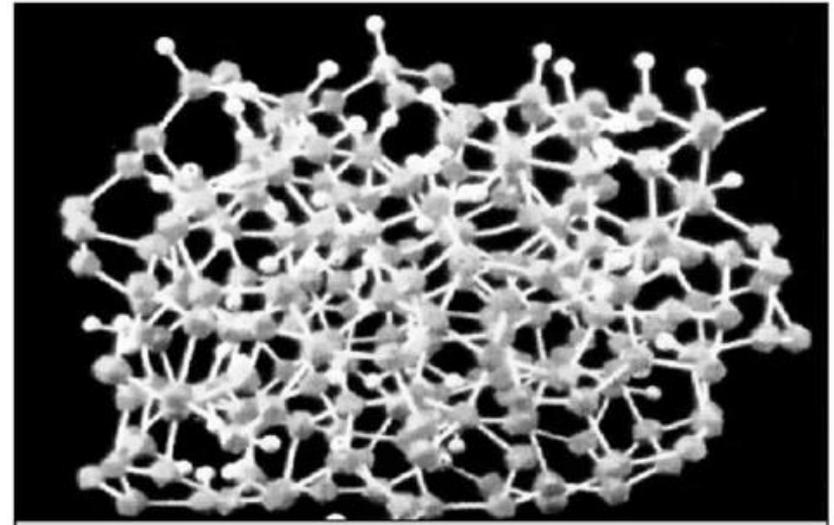
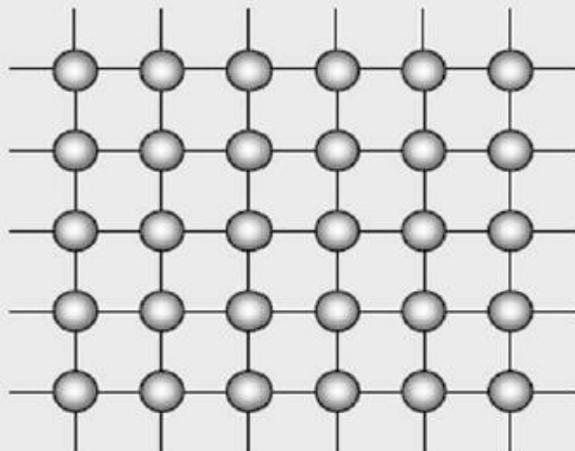


- Thin-film solar cells required a shorter value chain
- Long value chain presents difficulties with reaching grid parity.

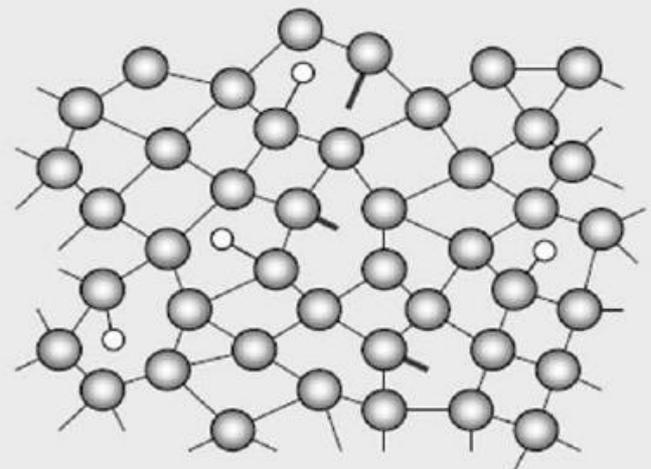
# Comparison of Atomic Structure of c-Si and a-Si:H



**Single crystal silicon**



**Hydrogenated amorphous silicon**



# Device structure of amorphous Si solar cell

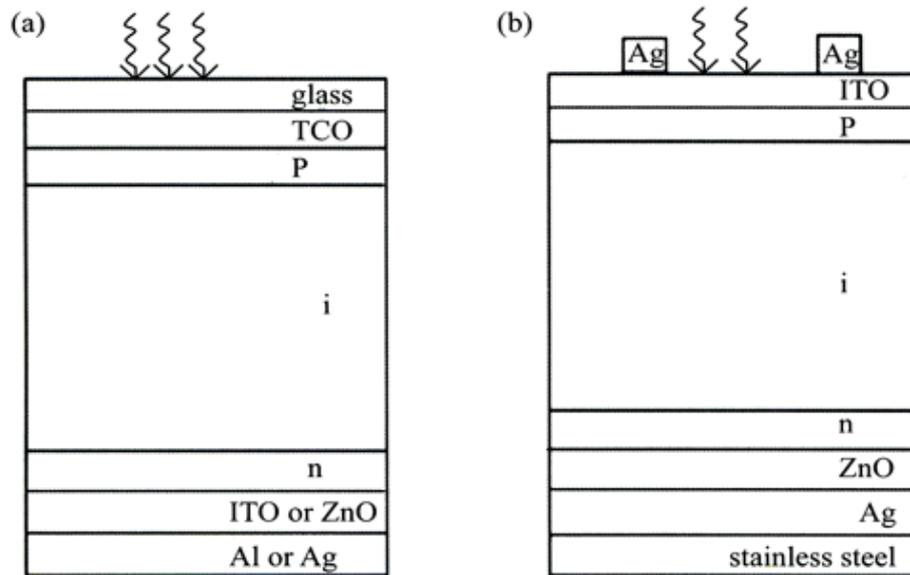
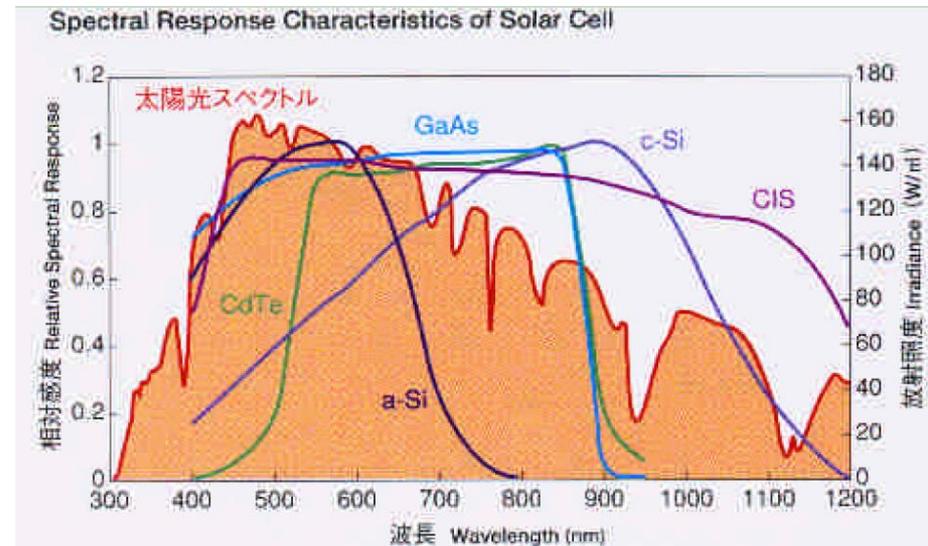


圖 6.7 典型 a-Si:H p-i-n 單界面太陽電池

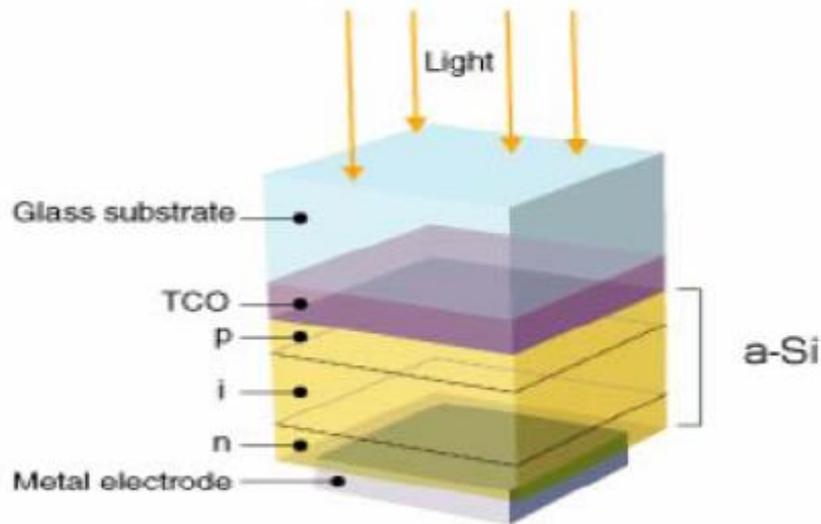
(a)superstrate 型式 , (b) substrate 型式



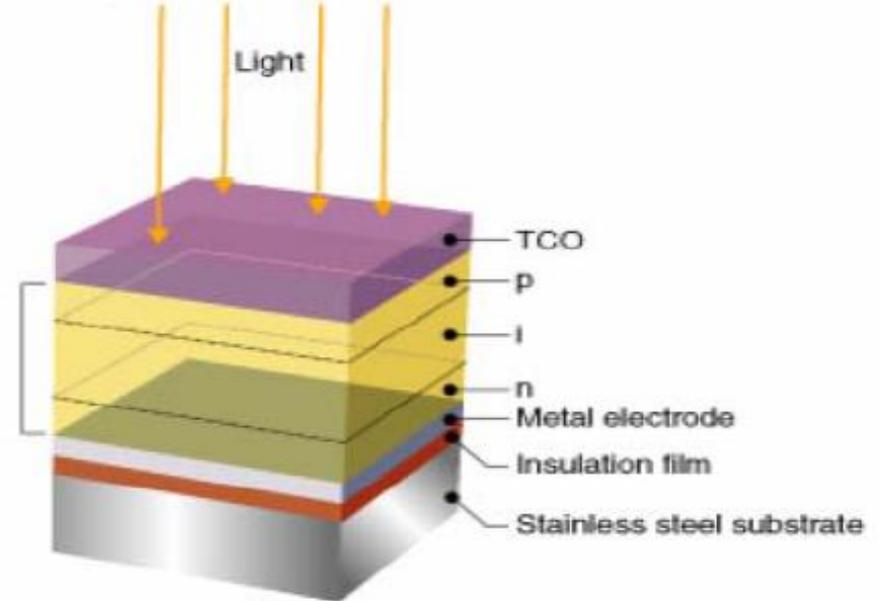
-a-Si's absorption coefficient at visible light is one order of magnitude than c-Si , so **only 10-30 nm** is needed to capture most photon in the visible region  
 -thickness of p and n type are around 10-30nm, thickness of i layer is less than 500 nm

# Structure of an amorphous Si solar cell

Glass/TCO/p-SiC/i-Si/n-Si/metal



TCO/p-SiC/i-Si/n-Si/metal/IL/SUS



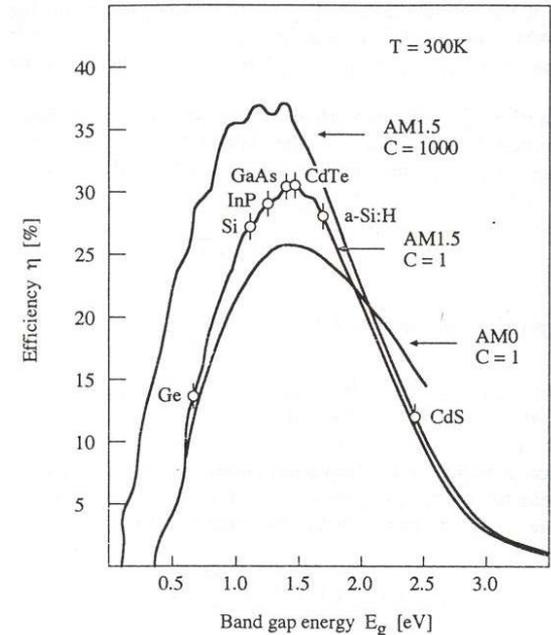
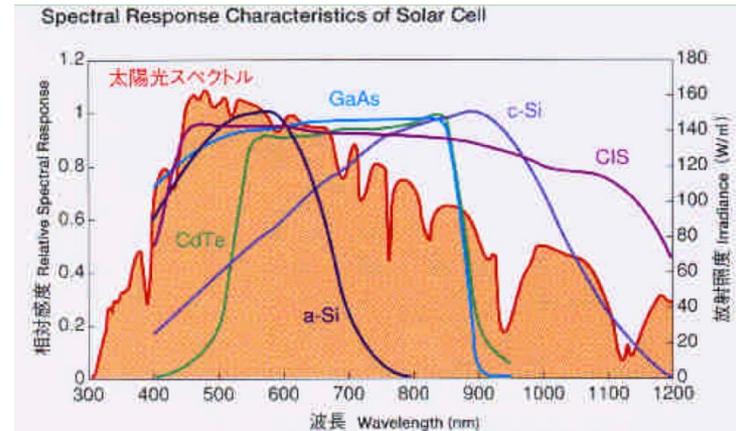
# II-VI compound solar cell: CdTe

**Abbreviated periodic table**

I	II	III	IV	V	VI
		B	C	N	O
		Al	Si	P	S
Cu	Zn	Ga	Ge	As	Se
Ag	<b>Cd</b>	In	Sn	Sb	<b>Te</b>

**II-VI group as light harvesting materials**

**band gap of CdTe : 1.5 eV**



# Device structure of a CdTe solar cell

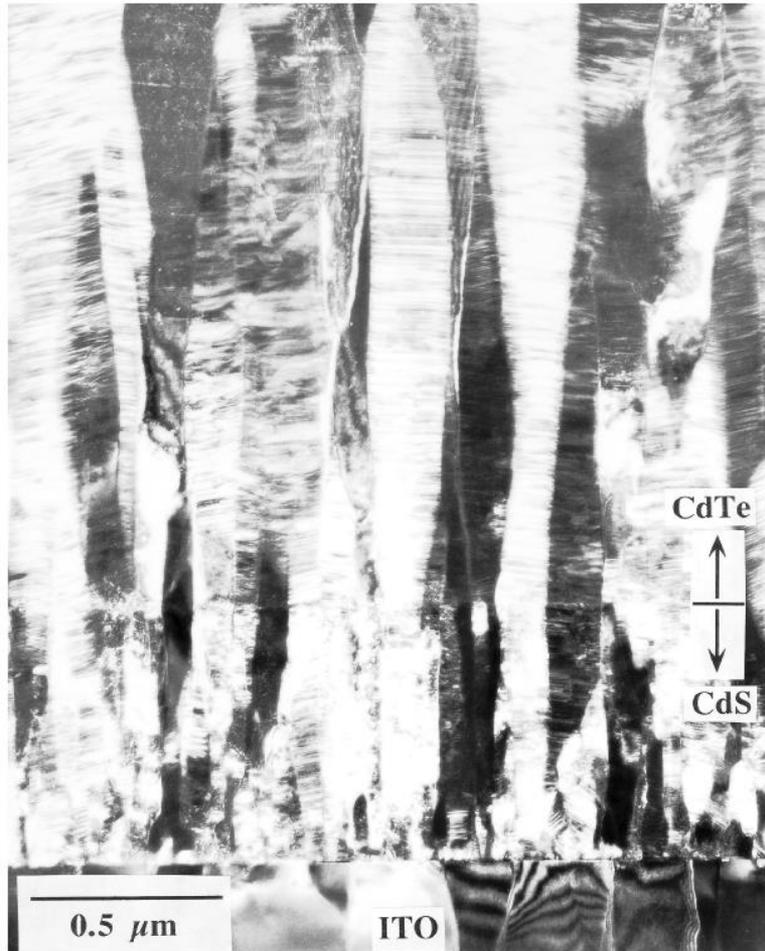


Figure 9 TEM micrograph showing the as deposited cross section of ITO/CdS/CdTe structure. Both CdS and CdTe are vapor deposited from the respective compounds.

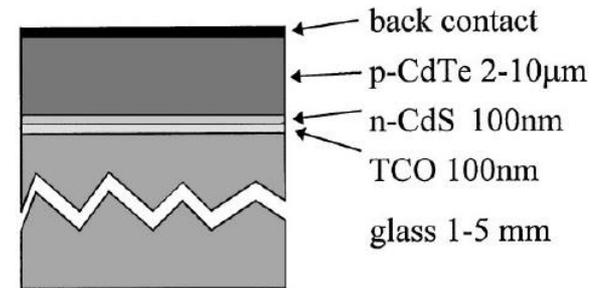
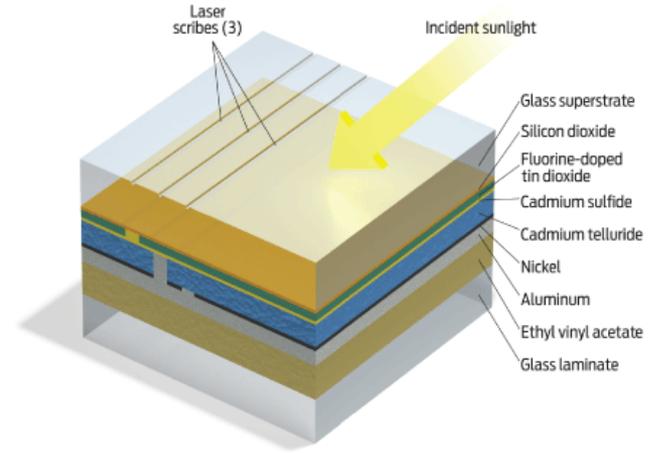


Fig. 1. The 'superstrate' configuration used for CdTe/CdS heterojunction solar cells.

**P-type: CdTe**  
**n-type: CdS**

# First solar

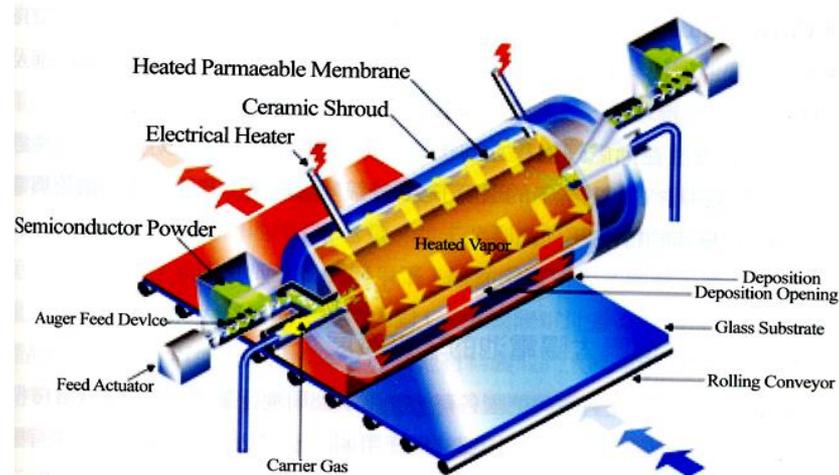


圖 7.8 First Solar 公司之量產型 CdTe 蒸氣輸送鍍膜示意圖

Pallas 綜合外電報導 / SolarExchange

**美國太陽能廠商First Solar於去年第四季生產成本降到每瓦0.98美元**

美國太陽能模組製造商First Solar ( Nasdaq: FSLR ) 於24日宣布，2008年第四季平均生產成本降到每瓦0.98美元，成為首家低於每瓦1美元的太陽能廠商。美國太陽能模組製造商First Solar ( Nasdaq: FSLR ) 於24日宣布，2008年第四季平均生產成本降到每瓦0.98美元，成為首家低於每瓦1美元的太陽能廠商。

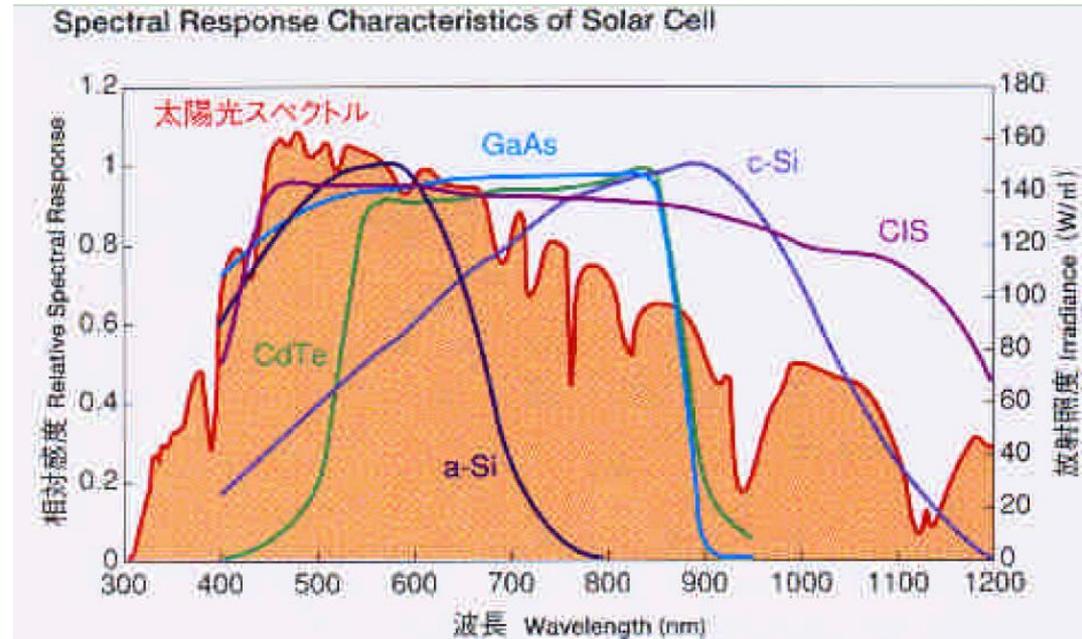
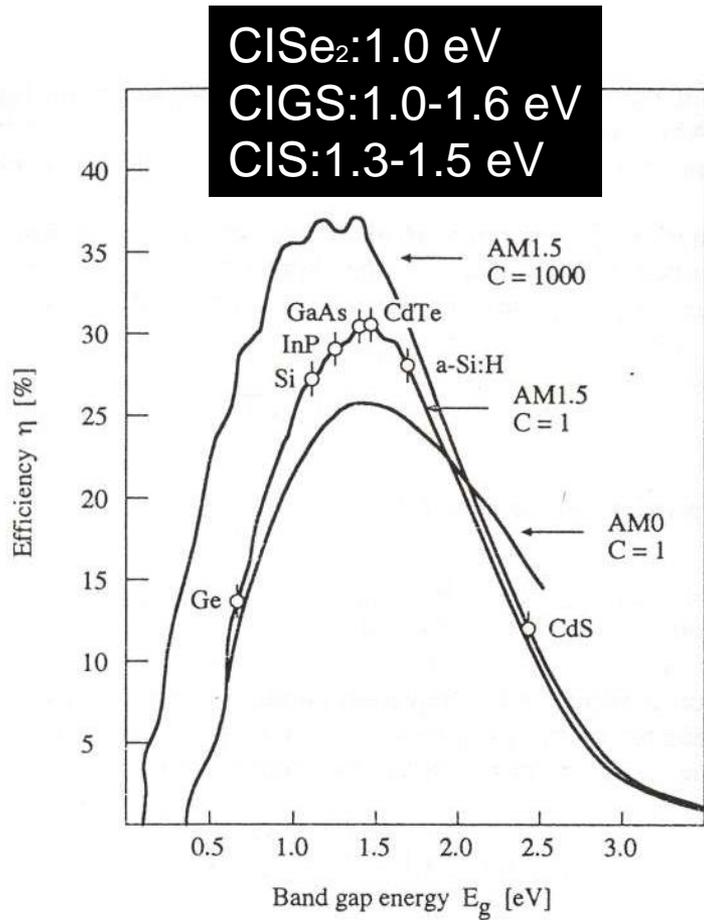
First Solar於2004年開始商業化生產以來，2008年產能成長約2500 % 達到500 MW 。

**2009年產能將再增加一倍，達到1GW。這相當於一般核電廠的規模。**

這些成長率隨著成本迅速降低而提高。生產成本自2004年以來降了超過三分之二，從每瓦3美元降到每瓦1美元不到。First Solar有信心能基於First Solar未用盡的技術和製程的潛力，進一步再將成本降至更低。

Current production  
Cost of crystalline  
Solar cell is around  
US 2/Wp

# Band gap and optical absorption of CIGS



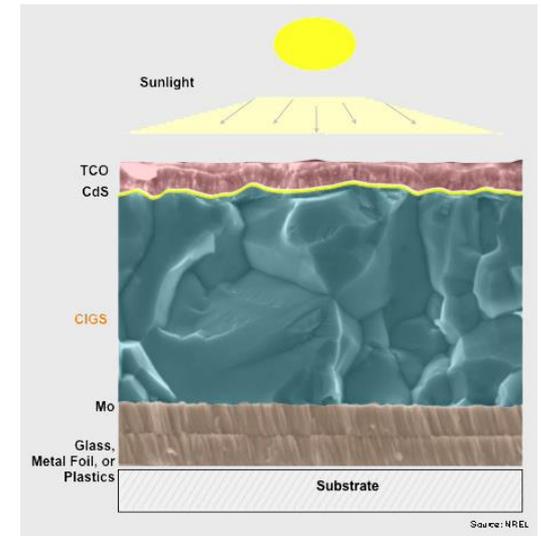
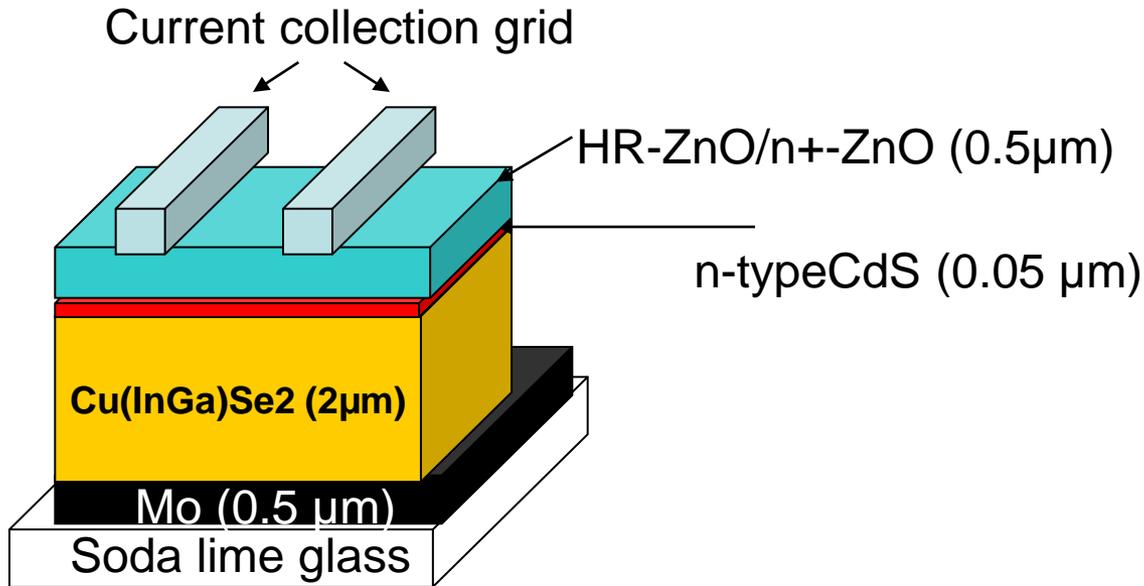
Muller, semiconductor for solar cells, 1993

$$\text{Efficiency} = \frac{FFV_{oc}I_{sc}}{P_{in}}$$

-CIGS's band gap is in the range of 1.1 to 1.5 eV

# I-III-V<sub>2</sub> compound solar cell: (CIGS)

Schematic picture



total device thickness less than 5 μm (Crystalline Si module ~200 μm)

# Compared to CdTe solar cells

Schematic picture

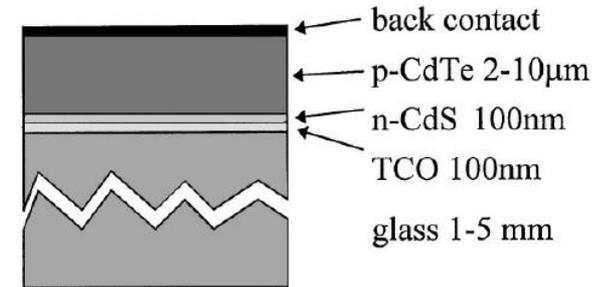
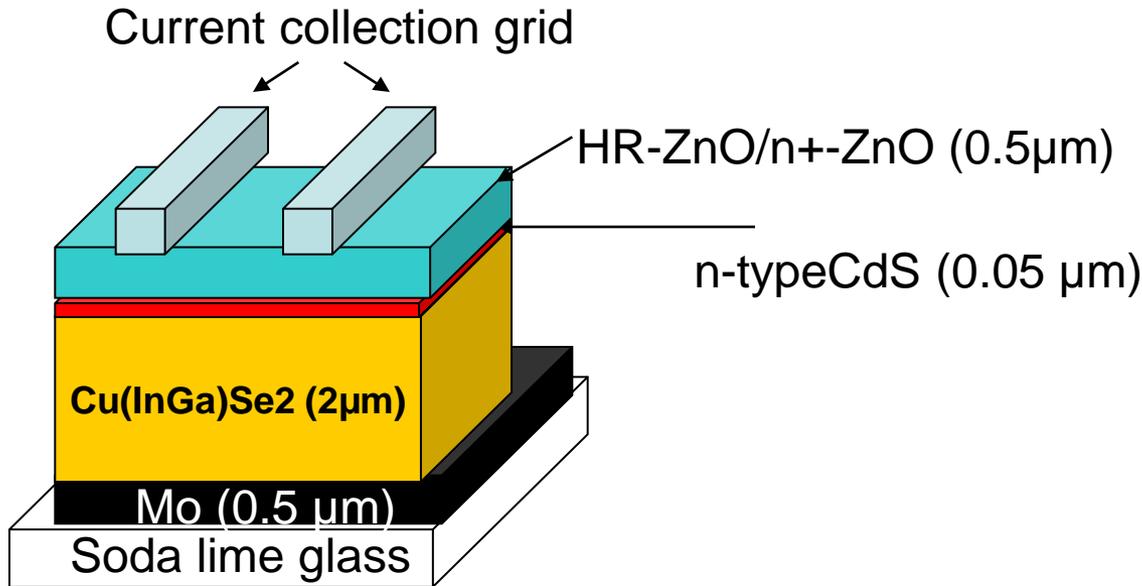
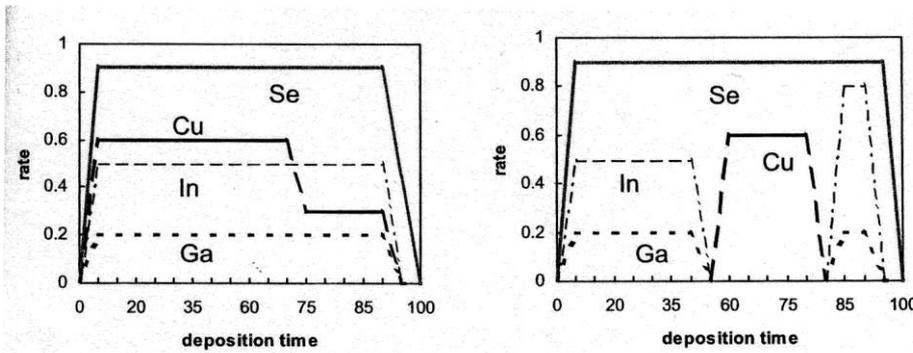
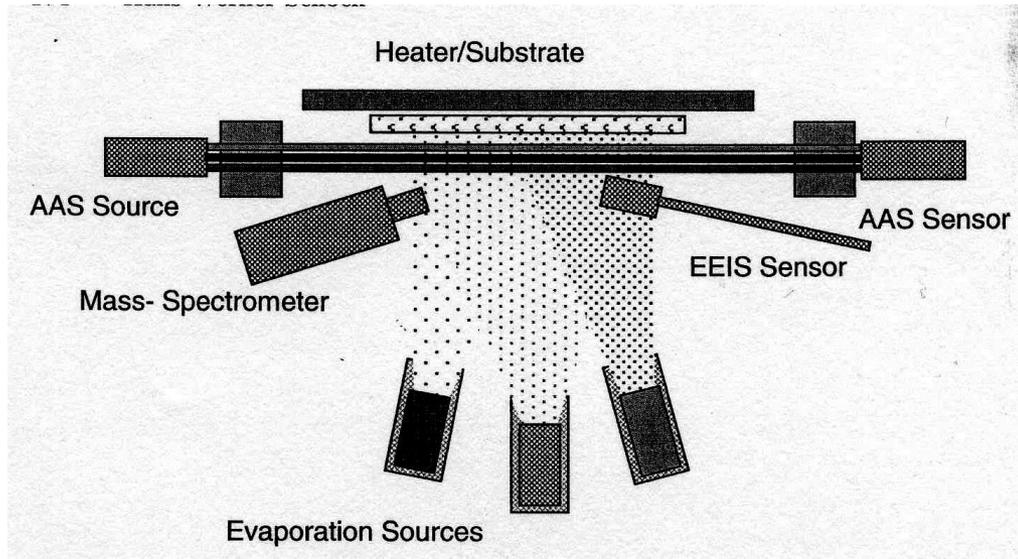


Fig. 1. The 'superstrate' configuration used for CdTe/CdS heterojunction solar cells.

# Much less Cd required

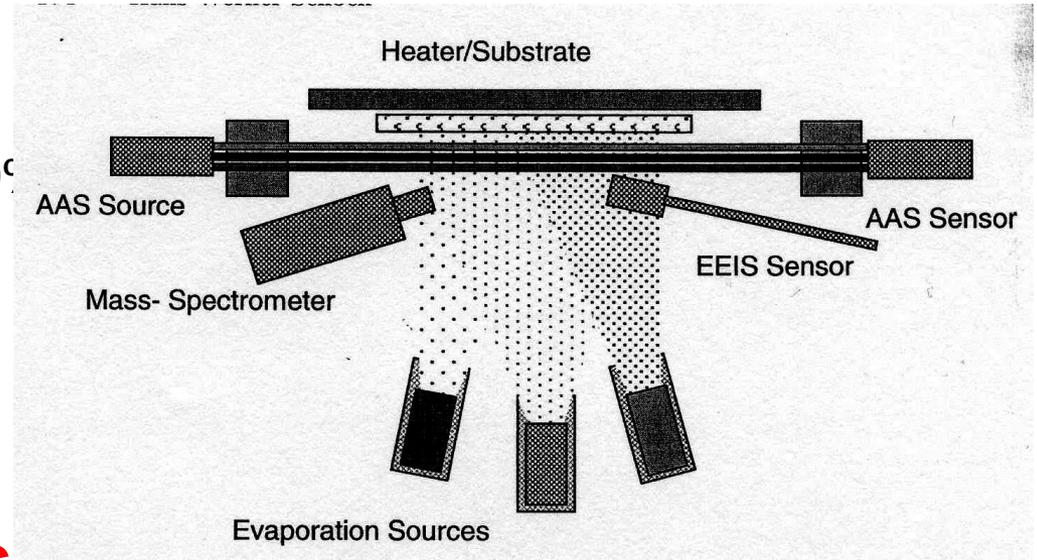
# An example of vacuum-based CIGS film deposition



Y. Hamakawa Thin-film solar cells,

# Vaccum-based CIGS film deposition

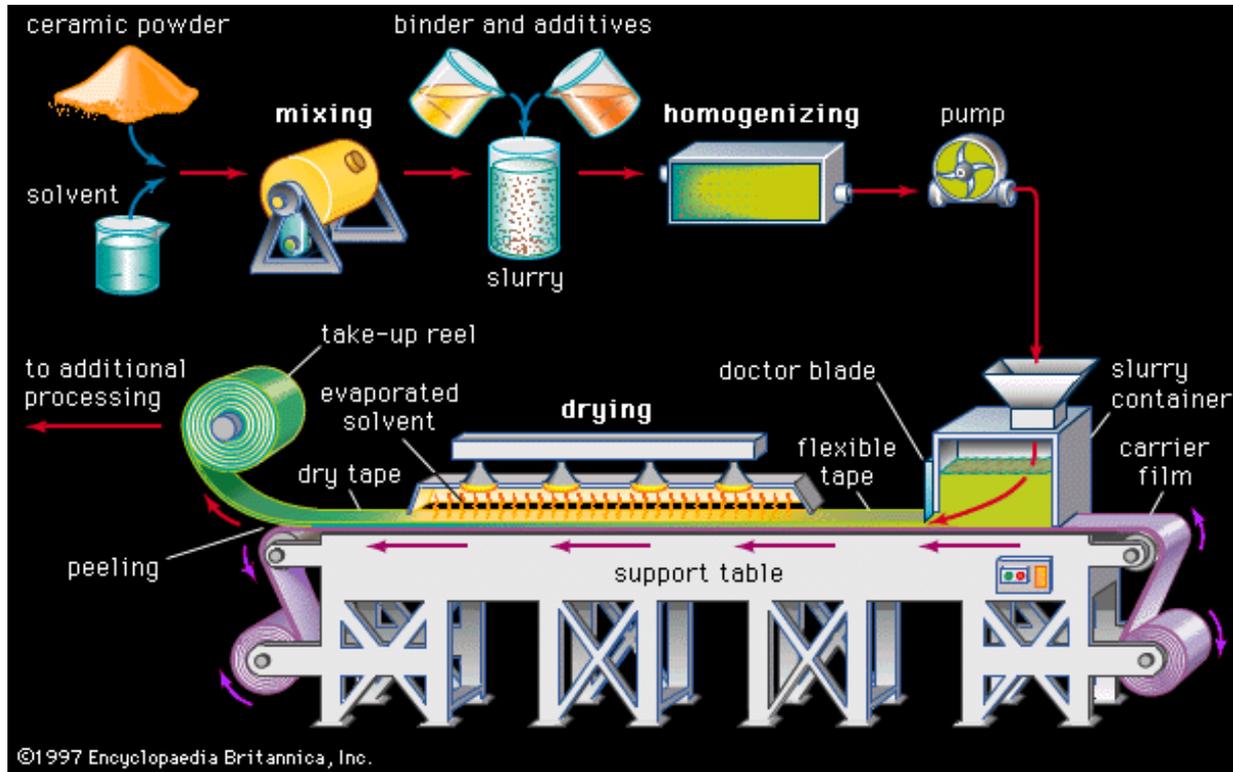
- Highest efficiency (lab scale: 18~20%)
- Usually UHV/MBE
- Cost prohibitive (but <cryst-Si)



## General drawbacks.

- Difficult to achieve controlled-stoichiometry over large device areas
- Manufacturing equipment is “very” expensive (> NT 0.1 billion)
- The deposition process is time-consuming
- Poor materials utilization (30-50%)
- Low throughput

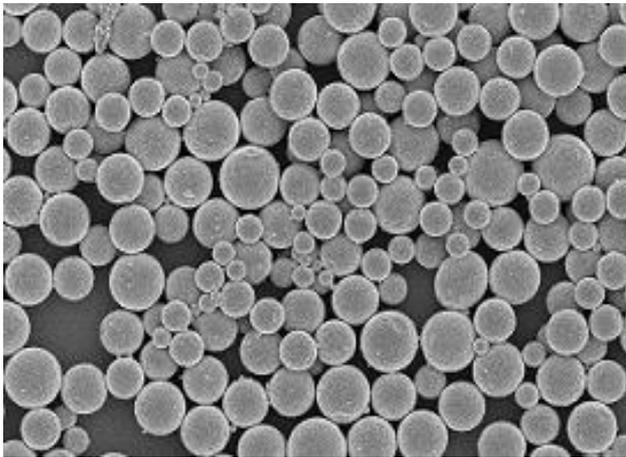
# Non-Vacuum Processing



- Synthesize colloidal nanocrystals with controlled CIGS stoichiometry and deposit layer
- Roll-to-roll manufacturing process

# Nano solar- Nanoparticle as ink for printable solar cell

CIGS particle ink



Roll-to-roll processing



Flexible solar cell

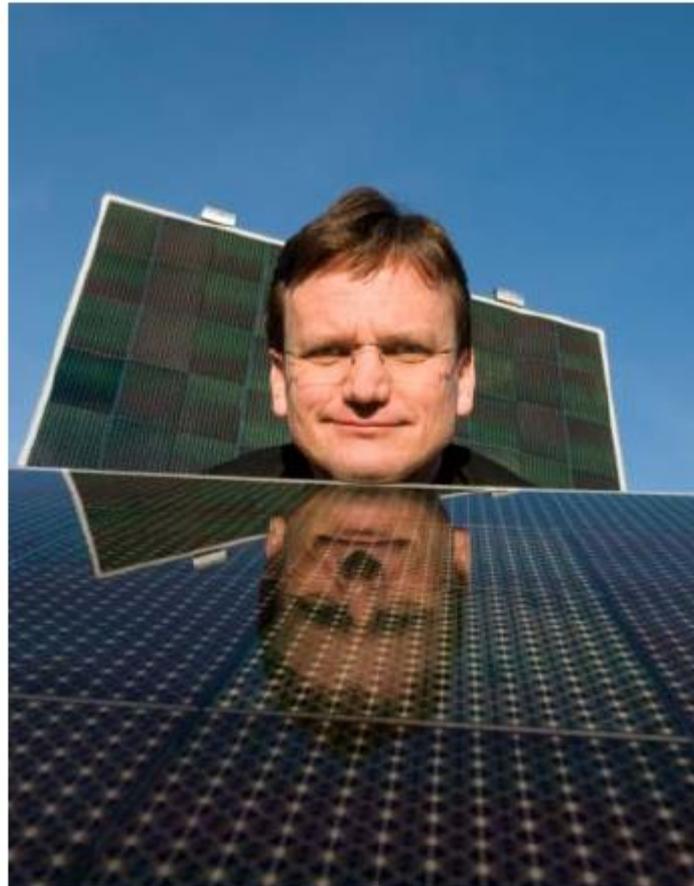


Add a video

has denonstrated a 1GW coater in a movie

	Silicon Wafer cells	Vacuum- based thin film	Roll-printed thin film
Process	Si wafer processing	High vacuum deposition	Roll-to-roll printing
Process Yield	Robust	Fragile	Robust
Materials Utilization	30%	30-60%	Over 97%
Throughput	1	2-5	10-25

# Nanosolar Ships First Panels - Dec. 18, 2007



Martin Roscheisen,  
CEO

首批的三片量產太陽能板一片將掛在公司裡做「精神堡壘」，一片捐贈給聖荷西的科技博物館，一片拿到 eBay 上拍賣，起價 0.99 美元，3 天後叫價到 13,000 美元。

# **Nanosolar, Thin-Film Solar Hype Firm, Officially Dead**

U.S. CIGS solar assets are being auctioned off after more than \$400 million in VC investment.

by Eric Wesoff

July 12, 2013

# CIGS Rocks! 2010/July

【時報記者沈培華台北報導】台積電 (2330) 新事業總經理蔡力行表示，台積電將以CIGS薄膜產品進軍太陽能產業，以五年期間朝全球前五大廠邁進，產能規模將達1GW規模，並看好此事業對台積電是有獲利與高成長潛力的新事業。台積電今天舉行先進薄膜太陽能技術研發中心暨先期量產廠房動土典禮。新事業總經理蔡力行表示，全球太陽能電池市場將持續成長，預期2009年至2015年全球太陽能電池市場年複合成長率可望達23%；其中，銅銦鎵硒(CIGS)因具有薄膜的低成本價格等優勢，成長率將最高，年複合成長率將達115%。台積電因此將以CIGS薄膜產品為主力，進軍太陽能產業。台積電先進薄膜太陽能廠第一期將投資約79.2億，預計2012年量產200百萬瓦(MW)，終期產能為700百萬瓦(MW)。台積電董事長張忠謀並預估，2015年太陽能佔台積電營收比重可望達10%。蔡力行表示，台積電三年內CIGS薄膜太陽能電池模組轉換效率將達14%，產能規模將約300至500百萬瓦，預期3至5年轉換效率將進一步提升至16%，產能規模將達1GW規模。

# 薄膜太陽能翻身 台積電產能衝3倍 全球龍頭廠轉盈 產業前景漸撥雲見日 2014/Feb

台積電旗下銅銦鎵硒(CIGS)薄膜太陽能廠日前年產能為40百萬瓦(MWp)，隨著整體市況轉佳、訂單滿載且技術獲得重大突破，台積電第4季CIGS年產能將擴增到120MWp，達到3倍規模，太陽能業者透露，以台積電在太陽能領域穩紮穩打風格來看，這次產能出現大躍進，凸顯CIGS接單情況明顯轉佳，業界紛預期台積電太陽能事業可望邁向獲利。不過，台積電發言體系表示，目前針對財務部分不予置評。

全球最具代表性的CIGS薄膜太陽能龍頭大廠是日本昭和殼牌石油旗下子公司Solar Frontier，年產能規模達900MWp，近年來一直陷入虧損困境翻不了身，然近期財報終於首度轉虧為盈，2013年旗下產能全數滿載，年營收暴增8成，並計劃在日本東北(Tohoku)擴增150MWp產能的CIGS新廠，預計2015年投產。

# 台積電太陽能步上關廠 業界:成本難敵 矽晶 2015/08/25 16:36

（中央社記者張建中新竹25日電）台積電100%持股子公司台積電太陽能將於8月底結束工廠營運。業界人士認為，薄膜太陽能電池成本難敵矽晶太陽能電池，是迫使台積電太陽能走向關廠的主因。

台積電新事業發展接連遭逢重大挫敗，旗下台積電固態照明因較晚進入發光二極體（LED）產業，業界專利障礙與通路開發不易，考量短期難以轉盈，台積電今年初決定將台積電固態照明全部股份賣予晶電。

台積電今天又宣布，旗下台積電太陽能因是市場後進者，缺乏經濟規模，雖然轉換效率具領先優勢，但在成本上不具競爭力，即便執行最精進的成本減抑計畫，也將難以逆轉成本劣勢，將於8月底結束工廠營運。

台積電是於2009年成立新事業部，並陸續成立台積電固態照明與台積電太陽能，分別投入LED與太陽能產業，由前總執行長蔡力行領軍。隨著蔡力行轉往中華電信擔任董事長，台積電固態照明與台積電太陽能董事長由左大川接任。

台積電太陽能成立之初，蔡力行曾表示，台積電太陽能將以技術領先為主要策略。

# 碩禾電子材料 太陽能電池導電膠

## 碩禾導電漿在全球產業地位

項目	全球市占 (%)	台灣市占 (%)	挑戰目標 (%)	產業地位	主要競爭者
背鋁	35	90	40	全球前2名	中國儒興
背銀	25~30	60	40	全球前3名	杜邦、中國中型廠
正銀	1	3~4	5	起步中	杜邦、賀利氏

註：背鋁及背銀目標為中期目標，為期2到3年；正銀目標為今年  
資料來源：碩禾 簡永祥／製表

碩禾 (3691) 股價560.0

## 碩禾相關權證

標的證券	權證代號	權證名稱	價內外 (%)	權證單價 (元)	剩餘天數	標的上漲1%、權證相對漲跌幅(%)
碩禾	706875	群益TK	內11.48	2.43	132	3.57

群益金鼎證券／製表

# 清大化工系友在光電業界的發展

三大上游材料，都是清大幫天下一清大幫創業公司表

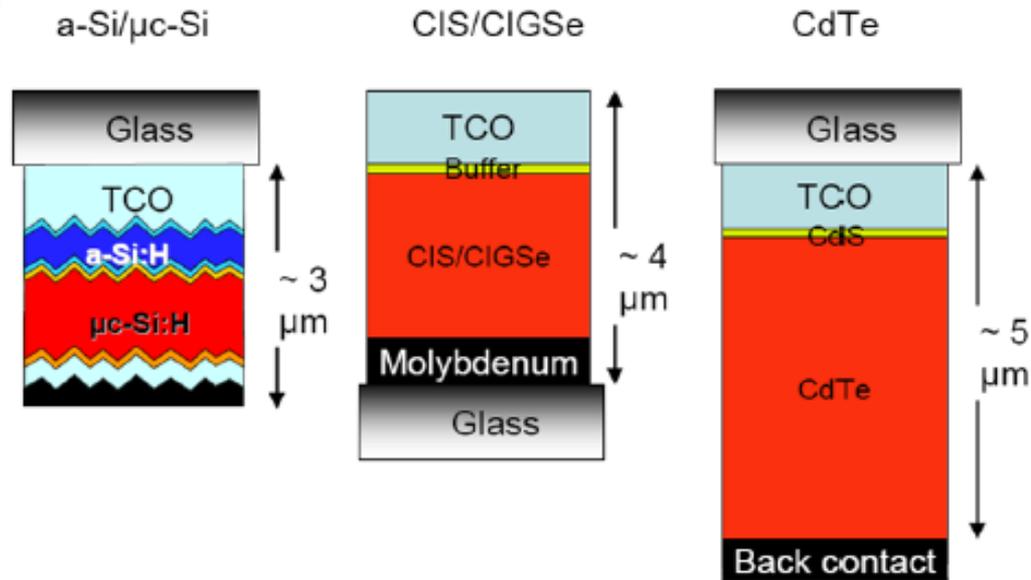
公司名：晶電代表人物：董事長李秉傑畢業系級：1985年化工博士公司地位：台灣第一大LED磊晶廠

公司名：璨圓代表人物：董事長簡奉任畢業系級：1985年化工系公司地位：台灣第二大LED磊晶廠

公司名：上緯代表人物：董事長蔡朝陽畢業系級：1987年化工碩士公司地位：高性能樹脂材料大廠

公司名：碩禾代表人物：蔡禮全 化學工程公司地位：太陽能導電膠大廠 副總經理

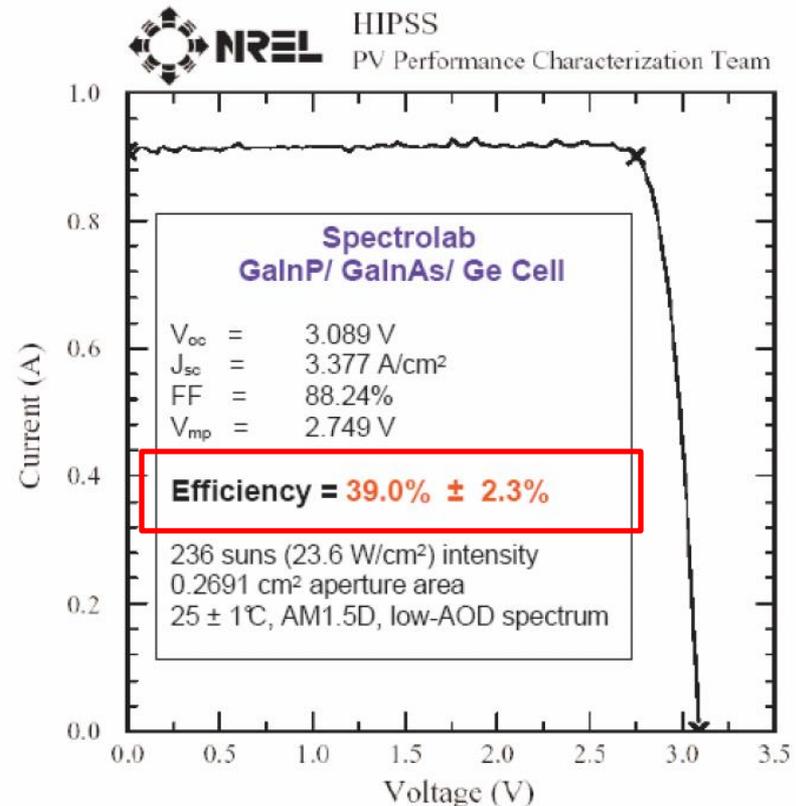
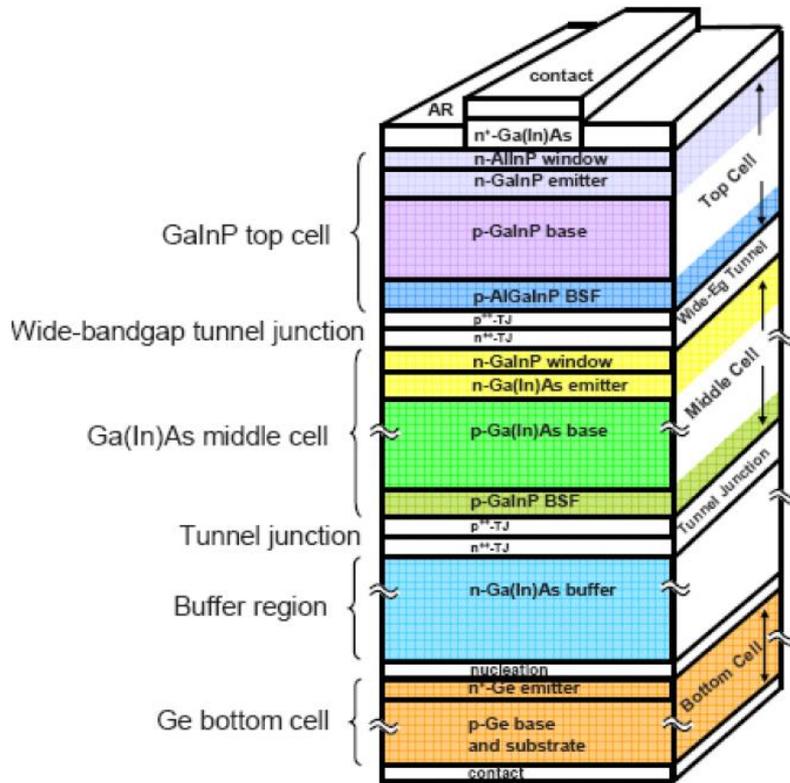
# Comparison of three thin film solar cells



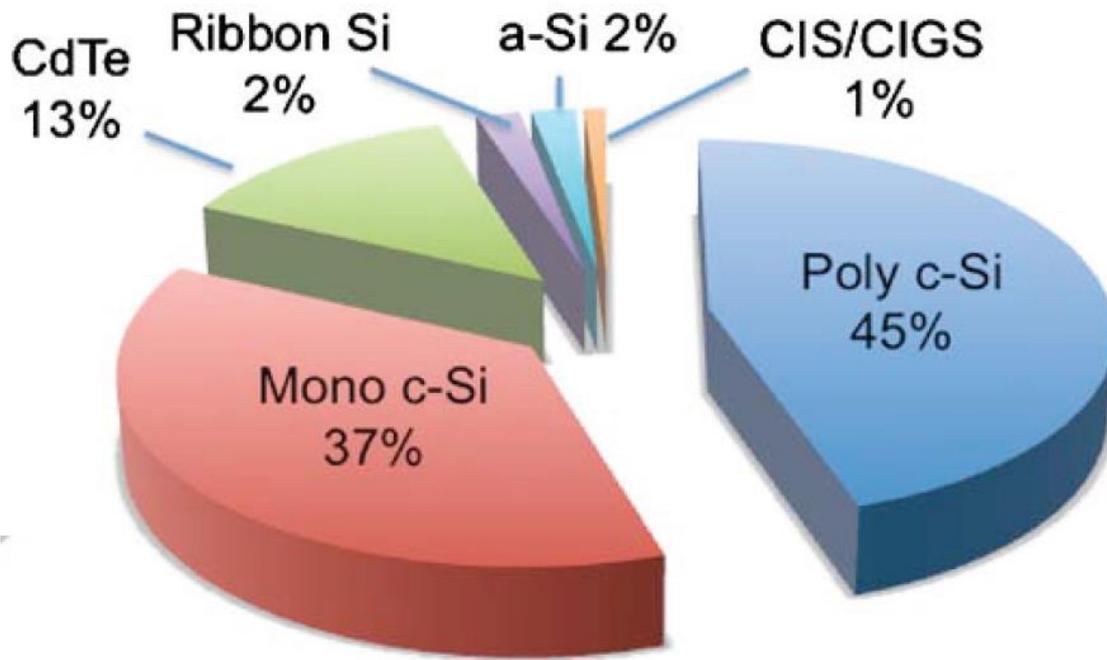
	a-Si/ $\mu$ c-Si	CIGS	CdTe
Junction	p/i/n, Si	p/n, CIGS/CdS	p/n, CdTe/CdS
Absorber	i-Si	P-CIGS.	P-CdTe
TCO	Textured FTO, AZO,	AZO	FTO, ITO
Eff. % in prod. (Lab)	$\sim 8.5$ ( $\sim 13$ )	10 $\sim$ 11.5 (19.9)	8.5 $\sim$ 10 (16.5)

# Tandem Junction Solar cells

## GalnP/GaAs/Ge Triple Junction Cell



# Market distribution in 2009



Total of 7.86 GW

- Both poly-Si and mono-Si combined a total 82%
- First Solar's CdTe was the first company to exceed 1GW/yr production, captured 13% market share.
- CIS/CIGS only has 1% market share.