

## Nanowires

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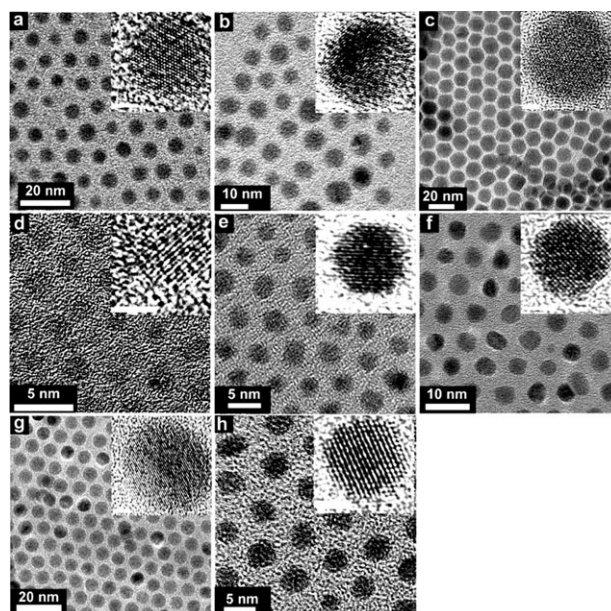
## Nanocrystal-Mediated Crystallization of Silicon and Germanium Nanowires in Organic Solvents: The Role of Catalysis and Solid-Phase Seeding\*\*

Hsing-Yu Tuan, Doh C. Lee, and Brian A. Korgel\*

The size-dependent properties, large surface-area-to-volume ratios, dispersibility in solvents, and mechanical flexibility of semiconductor nanowires make them an exciting class of materials.<sup>[1–3]</sup> Silicon nanowires in particular can be both *n* and *p*-doped and interface well with an insulating oxide and conducting metal silicide and these nanowires might be applied in silicon CMOS (complementary metal oxide semiconductor) circuits or with organic compounds on flexible plastic substrates.<sup>[4,5]</sup> Germanium is similar to silicon in many respects, but with a higher carrier mobility. Gold-seeded vapor-liquid-solid (VLS) growth is a common route to silicon and germanium nanowire synthesis at low growth temperatures ( $\approx 360^\circ\text{C}$ ).<sup>[1,3,6–8]</sup> Unfortunately, gold traps electrons

and holes in both silicon and germanium and poses a serious contamination problem for nanowire integration with silicon CMOSs. Surprisingly few gold alternatives have been investigated for nanowire seeding. Si nanowires have been seeded by chemical vapor deposition (CVD) with Ti particles<sup>[9]</sup> and Ga droplets,<sup>[10]</sup> and in organic solvents, Si and Ge nanowires were synthesized using Ni nanocrystals.<sup>[11,12]</sup> Gold alternatives have been explored more extensively for other semiconductors: Sn for ZnO wires,<sup>[13]</sup> various metal films for vapor-grown tin oxide nanowires,<sup>[14]</sup> and low-melting metals such as In and Bi for solution synthesis of Group II–VI,<sup>[15,16]</sup> III–V<sup>[17–19]</sup> and Ge nanowires.<sup>[20]</sup> Herein, many different nanocrystals—Co, Ni, CuS, Mn, Ir, MnPt<sub>3</sub>, Fe<sub>2</sub>O<sub>3</sub>, and FePt—are explored as seeds for Si and Ge nanowire synthesis to develop a more general understanding of the role of the seed particles in the nanowire growth process.

Si and Ge nanowires were grown by decomposing silanes or germanes in high temperature (450–500°C), high pressure (10.3 MPa) supercritical toluene.<sup>[21,22]</sup> Under these reaction conditions, Au nanocrystals seed nanowires by the “super-critical fluid-liquid-solid” (SFLS) mechanism in which nanowires evolve from a liquid Au:Si (or Au:Ge) eutectic. In combination with Si and Ge, many of the seed materials studied herein do not form liquid eutectics until reaching temperatures well above 500°C, and are not expected to work for “VLS”-like growth. However, they form solid alloys below 500°C, perhaps making solid-phase nanowire seeding possible. Figure 1 shows TEM images of the various colloidal nanocrystals used to seed Si and Ge nanowires; their size distributions had standard deviations less than 20% about mean diameters ranging between 4.2 and 10.2 nm. Figure 2 shows the reaction products. All of the nanocrystals seeded Si and Ge nanowires from monophenylsilane (MPS) and diphenylgermane (DPG), but with varying success (summar-



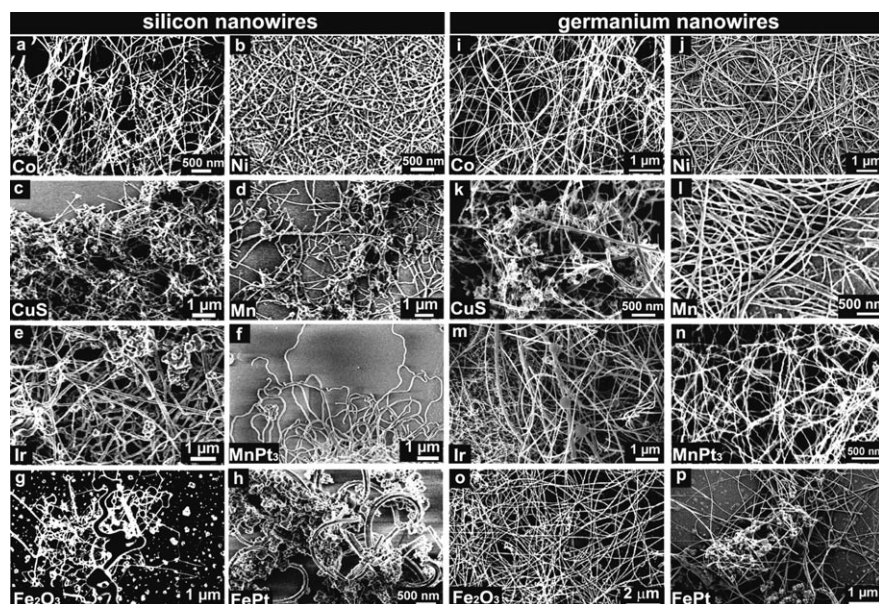
**Figure 1.** TEM images of metal nanocrystals studied as Si and Ge nanowire seeds: a) Co, b) Ni, c) CuS, d) Mn, e) Ir, f) MnPt<sub>3</sub>, g) Fe<sub>2</sub>O<sub>3</sub> and h) FePt. Insets: magnified images, scale bars 2 nm.

[\*] H.-Y. Tuan, D. C. Lee, Prof. B. A. Korgel  
Department of Chemical Engineering  
Texas Materials Institute  
Center for Nano- and Molecular Science and Technology  
The University of Texas  
Austin, TX 78712-1062 (USA)  
Fax: (+1) 512-471-7060  
E-mail: korgel@mail.che.utexas.edu

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**Figure 2.** SEM images of a–h) Si and i–p) Ge nanowires synthesized in supercritical toluene from MPS (150 mm, 500 °C, 10.3 MPa) and DPG (80 mm, 460 °C, 10.3 MPa), respectively.

ized in Table 1). In general, straight nanowires are crystalline with few extended defects, whereas curly wires are usually amorphous or polycrystalline.<sup>[23]</sup> Co nanocrystals gave the highest yield of straight, long (> 10  $\mu\text{m}$ ) Si and Ge nanowires (Figure 3). Ni nanocrystals also produced crystalline Si and Ge nanowires with good yield. CuS nanocrystals produced straight crystalline Si nanowires with good yield but slightly

alloys with Si and Ge at the growth temperatures and energy dispersive spectroscopy (EDS) analysis of the seed particles found at the tips of many nanowires revealed silicide and germanides (see Supporting Information for representative data). In the particles located at the tips of nanowires seeded with CuS and  $\text{Fe}_2\text{O}_3$  nanocrystals, S and O were not observed in significant quantities, indicating that wires probably

shorter lengths (3–10  $\mu\text{m}$ ) and  $\text{Fe}_2\text{O}_3$  nanocrystals produced high quality Ge nanowires with relatively high yield.

All of the nanocrystals produced nanowires at temperatures significantly below their bulk eutectic temperatures (summarized in Table 1). The small size of the seed nanocrystals reduces the eutectic temperature; however, a drop of nearly 350 °C is unlikely.<sup>[12]</sup> These nanocrystals most likely promote nanowire crystallization from solid-phase seeds. Solid-phase metal-seeded nanowire growth has also been proposed in other systems: Si (Ti,<sup>[9]</sup> Ni<sup>[11]</sup>), Ge (Fe,<sup>[24]</sup> Ni<sup>[12]</sup>), GaAs (Au),<sup>[25]</sup> InAs (Au),<sup>[26]</sup> and ZnSe (Au,<sup>[27]</sup> Fe<sup>[28]</sup>) nanowires. Solid-phase seeding is a viable growth mechanism, provided that the seed particles are small enough for rapid saturation by solid-state diffusion and the semiconductor has a high solid solubility in the metal. Co, Ni, Fe, and Cu all form

crystallize from copper silicide and iron germanide phases. Perhaps CuS and  $\text{Fe}_2\text{O}_3$  are first reduced to Cu and Fe metal in the reaction mixture, prior to nanowire growth. Particles found at the tips of FePt-seeded nanowires showed all three elements—Fe, Pt, and Ge—indicating that nanowire growth probably originates from a ternary Fe:Pt:Ge phase.

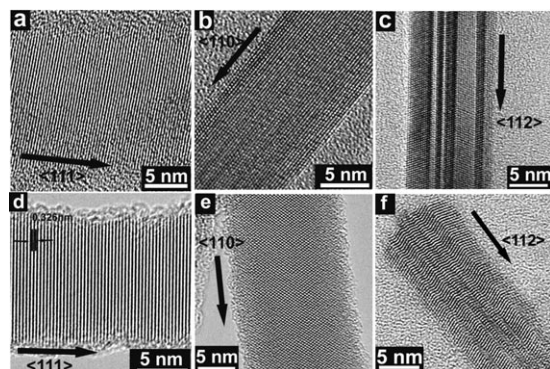
Si and Ge nanowires seeded with Ni, Co,  $\text{Fe}_2\text{O}_3$ , and CuS show two predominant growth directions— $\langle 111 \rangle$  and  $\langle 110 \rangle$ —in nearly equal proportions (Figure 3). Approximately 5% of the nanowires also had  $\langle 112 \rangle$ -oriented growth. This result is different from that for Au-seeded Si and Ge nanowires in organic solvents, which show preferential growth in either the  $\langle 111 \rangle$  (Si) or  $\langle 110 \rangle$  (Ge) directions, with only a small proportion of  $\langle 110 \rangle$  (or  $\langle 111 \rangle$ ) and  $\langle 112 \rangle$  orientations.<sup>[22,29]</sup> Perhaps the difference in nanowire growth direction relates to the solid-phase

**Table 1:** Summary of seed nanocrystal composition and selected properties and reaction conditions.

Nanowire	Seed	Nanocrystal diameter [nm]	Reaction temp. [°C] <sup>[a]</sup>	Eutectic temp. [°C] <sup>[h]</sup>	Product description
Si	Co	9.7	500 <sup>[b]</sup>	1200	Long straight wires
Si	Ni	5.6	460	964	Long straight wires
Si	CuS	11.7	500	802 <sup>[d]</sup>	Good yield, shorter wires
Si	Mn	4.7	500	1040	Crystalline wires, low yield
Si	Ir	4.2	500	1470	Amorphous particles & curly wires
Si	MnPt <sub>3</sub>	5.1	500	N/A	Low yield, mostly curly wires
Si	$\text{Fe}_2\text{O}_3$	10.2	500	1200 <sup>[e]</sup>	Amorphous particles & few wires
Si	FePt	4.3	500	N/A	Amorphous particles
Ge	Co	9.7	460 <sup>[c]</sup>	817	Long straight wires
Ge	Ni	5.6	460	762	Long straight wires
Ge	CuS	11.7	460	644 <sup>[f]</sup>	Short curly wires
Ge	Mn	4.7	460	720	Crystalline wires, low yield
Ge	Ir	4.2	460	N/A	Wires & amorphous particles
Ge	MnPt <sub>3</sub>	5.1	460	N/A	Straight wires with rough surfaces
Ge	$\text{Fe}_2\text{O}_3$	10.2	460	838 <sup>[g]</sup>	Long straight wires
Ge	FePt	4.3	460	N/A	Wires & amorphous particles

[a] The optimal synthesis temperature for Si nanowires was approximately 40 °C higher than for Ge nanowires in all cases, indicating slower Si nanowire growth kinetics. [b] The optimum reaction temperature for cobalt-seeded Si nanowires from MPS was approximately 50 °C higher than the optimal synthesis temperature (450 °C) using Au seeds. [c] The optimum reaction temperature for cobalt-seeded Ge nanowires from DPG is approximately 80 °C higher than the optimal synthesis temperature (380 °C) using Au seeds. [d] Value based on Cu:Si phase diagram, because S appears to outgas from the seed particles. [e] Value based on Fe:Si phase diagram, because iron silicide appears to be the seed composition. [f] Value based on Cu:Ge phase diagram, because S appears to outgas from the seed particles. [g] Value based on Fe:Ge phase diagram, because iron germanide appears to be the seed composition. [h] Ref. [42].



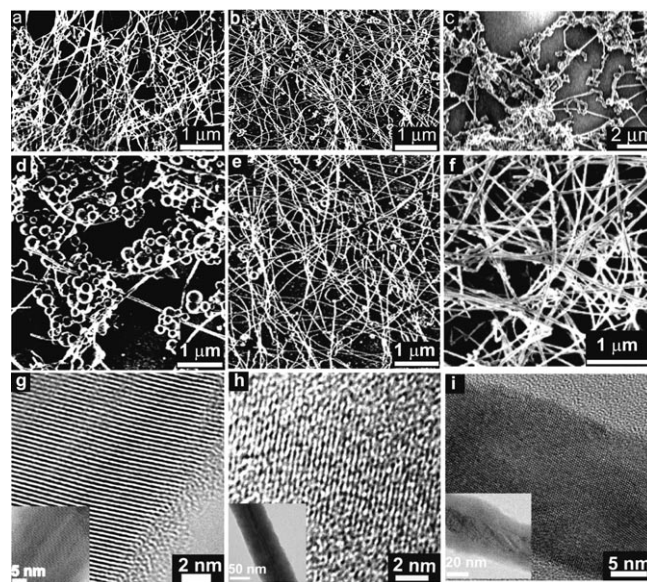


**Figure 3.** HRTEM images of Si (a–c) and Ge (d–f) nanowires seeded by Co (a–c),  $\text{Fe}_2\text{O}_3$  (d–e), and CuS (f) showing  $\langle 110 \rangle$ ,  $\langle 111 \rangle$  and  $\langle 112 \rangle$  growth directions. Approximately 5% of the sample contained  $\langle 112 \rangle$  oriented nanowires, usually with longitudinal  $\{111\}$  twins, as in (c) and (f). The 0.326 nm lattice spacing agrees with the  $\langle 111 \rangle$  d-spacing for bulk Ge (0.327 nm).

nanowire seeding process. However, it is noteworthy that regardless of the metal used to seed the nanowires (i.e., Au, Ni, or Co.),  $\langle 112 \rangle$ -oriented nanowires always tend to have longitudinal  $\{111\}$  twins, as in the wires in Figure 3c and f.

In VLS growth, the metal seed dissolves the semiconductor and recrystallizes it as a nanowire and has a passive role in the precursor decomposition chemistry.<sup>[3,6,22]</sup> Au does not catalyze reactant decomposition, but simply helps drive crystallization (Au could be called a “crystallization catalyst”, but not a catalyst for reactant decomposition). In contrast to Au, the transition metals Ni, Co, and Fe, are catalysts for molecular decomposition reactions. For carbon nanotube (CNT) growth, Fe, Co, and Ni are used as catalysts that enhance reactant decomposition at the seed surface, while also promoting “crystallization” of the nanostructure—for CNTs that is graphitization and tube growth.<sup>[30–32]</sup> Au-seeded CVD nanowire growth typically uses very reactive precursors, such as silane, and there is generally no need to use catalytic seed metals to promote reactant decomposition. However, sidewall deposition can occur with reactive precursors and lead to substantial diameter tapering over the length of the wire.<sup>[33,34]</sup> Catalytic seeds might be able to lower the growth temperature and help prevent sidewall deposition. In Au-seeded, solution-phase nanowire growth, many organosilane and organogermane reactants, such as octylsilane, are too unreactive to promote nanowire formation at typical reaction temperatures.<sup>[11,22]</sup> Trisilane on the other hand is too reactive and forms amorphous Si particles.<sup>[22]</sup> These Si reactants require a catalytic seed to promote nanowire formation.

Figure 4 shows TEM and SEM images of Si nanowires seeded by Co nanocrystals in supercritical toluene from MPS, trisilane, and octylsilane—all three reactants gave nanowires. The cobalt nanocrystals enhance octylsilane decomposition enough to promote nanowire formation; however, the nanowire quality is only marginal as the nanowires are coated with an amorphous layer with significant carbon content (Figure 4i, 3:1 C:Si by EDS in shell). The cobalt nanocrystals were found to promote Si nanowire formation using trisilane at temperatures as low as 350°C, although the majority



**Figure 4.** Cobalt-nanocrystal-seeded Si nanowires. HRSEM images of Si nanowires synthesized in supercritical toluene (10.3 MPa) from a) MPS (500°C), b) trisilane (400°C), and c) octylsilane (500°C). HRSEM images of Si nanowires synthesized in supercritical toluene (10.3 MPa) from trisilane at d) 350°C, e) 400°C, and f) 450°C. g–i) TEM images of Si nanowires synthesized in supercritical toluene (10.3 MPa) with g) trisilane (400°C), h) trisilane (450°C) and i) octylsilane (500°C). The nanowires in (h) and (i) are coated with an amorphous shell, as shown more clearly in the low-resolution TEM images in the insets in (h) and (i). The shell material in (h) is amorphous Si and in (i) it is amorphous 3:1 C:Si (by EDS).

product at this low temperature was amorphous Si colloids (Figure 4d). Cobalt enhances heterogeneous trisilane decomposition relative to homogeneous particle formation. Si nanowires produced with trisilane at 400°C had very little sidewall deposition and few particulates; slightly higher temperature (450°C) gave significant sidewall deposition (Figure 4h).

Of the nanocrystals studied, Co gave the highest yield and quality of both Si and Ge nanowires, rivaling Au-seeded reactions (See Supporting Information for an example of Au-seeded Si nanowires). All growth temperatures were well below the bulk eutectic temperatures, indicating solid-phase seeding. Solid-phase seeding can probably occur for any semiconductor with a high solubility in the seed metal; however, the growth temperature must be sufficiently high for fast saturation by solid-state diffusion, which can occur relatively fast in nanometer-diameter seed particles. Cobalt was also found to catalyze silane decomposition to promote Si nanowire growth from octylsilane and trisilane, similar to past studies on Ni nanocrystals.<sup>[11,12]</sup> In gas-phase reactions, the use of catalytic transition-metal seeds might enable lower temperature reactions for less sidewall deposition and better diameter control. As more seed materials are studied, the solid-phase growth method may become as prevalent as liquid-eutectic seeding, and the use of catalytic seed particles might serve as a general approach for improved diameter control, as it has for carbon nanotube growth.

## Experimental Section

Anhydrous toluene (99.8%, Sigma-Aldrich), monophenylsilane (MPS, 97%, Sigma-Aldrich), *N*-octylsilane ((C<sub>8</sub>H<sub>17</sub>)H<sub>3</sub>Si, Gelest), trisilane (Si<sub>3</sub>H<sub>8</sub>, Voltaix), and diphenylgermane (DPG, Gelest) were used as received. Co,<sup>[35]</sup> Ni,<sup>[36]</sup> CuS,<sup>[37]</sup> Ir,<sup>[38]</sup> Mn (Supporting Information), MnPt<sub>3</sub>, Fe<sub>2</sub>O<sub>3</sub>,<sup>[39]</sup> and FePt<sup>[40]</sup> nanocrystals were prepared by arrested precipitation as described in the literature.

Nanowires were synthesized in semibatch 1 mL or 10 mL Ti grade-2 reactor connected to a high-pressure liquid chromatography (HPLC) pump for pressure control and reactant injection. The reactor was heated in a brass block with temperature monitored by a thermocouple.<sup>[22,32,41]</sup> A small piece of clean Si wafer was placed in the reactor as a deposition substrate to gather reaction product. The reactor was sealed in a nitrogen-filled glove box, connected to the HPLC pump, and then filled with anhydrous oxygen-free toluene. The reactor was pressurized to 3.4 MPa with toluene and then heated to the reaction temperature. 500  $\mu$ L of 150 mM MPS in toluene with Si:nanocrystal mole ratios of 50:1 (Co), 100:1 (Ni), 150:1 (CuS), 50:1 (Mn), 50:1 (Ir), 50:1 (MnPt<sub>3</sub>), 50:1 (Fe<sub>2</sub>O<sub>3</sub>), and 50:1 (FePt), were injected from a six-way valve injection loop (Valco) at 0.4 mL min<sup>-1</sup>. For Ge nanowires, 500  $\mu$ L of 80 mM DPG in toluene were injected with Ge:nanocrystal mole ratios of 100:1 (Co), 100:1 (Ni), 100:1 (CuS), 50:1 (Mn), 20:1 (Ir), 100:1 (MnPt<sub>3</sub>), 200:1 (Fe<sub>2</sub>O<sub>3</sub>), and 200:1 (FePt). The reaction pressure was increased to 10.3 MPa with additional toluene. After 10 min, the reactor was immersed in ice-water and cooled to room temperature. The reactor was opened and the deposition substrate removed and stored under nitrogen until the product was characterized by SEM and TEM.

Nanowires were imaged by HRSEM on the deposition substrate without further purification using a LEO 1530 field-emission SEM at 1–3 kV accelerating voltage with working distances ranging between 2–5 mm. For HRTEM and STEM imaging, nanowires were either drop-cast from chloroform dispersions or dry transferred by scratching the deposition substrate onto 200-mesh lacey carbon-coated copper or nickel grids (Electron Microscope Sciences). Images were acquired using 200 kV accelerating voltage on a JEOL 2010F equipped with an Oxford INCA ED spectrometer.

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